6.111 Project Report: Sign Language Translator

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1 Introduction

American Sign Language (ASL) serves as a form of non-verbal communication for entire facets of society. However, the majority of Americans are not fluent in, let alone able to understand, ASL. This limits the means by which individuals that rely on ASL can communicate with the rest of the population. As such, we created a real-time sign language translator using the Nexys 4 DDR that recognizes the ASL alphabet. This alphabet consists of all 26 letters in the English alphabet, with 24 letters signed without motion and 2 letters signed including movement of the hand. While ASL letters are normally signed with the signer's dominant hand, our system only recognizes signs given with the right hand.

2 Materials

- Nexys 4 DDR FPGA
- OV7670 camera and ESP8266 microcontroller
- VGA compatible monitor
- Red, yellow, green, and blue LilyPad LEDs
- Conductive thread
- Coin-cell battery holder with switch
- CR2032 Batteries
- Black cotton gloves
- Black fabric paint
- Hot glue
- Assorted paper scraps

3 LED Glove



Figure 1: LED glove

To give our system a way to recognize points of interest on the user's hand, we designed and created an LED glove. Since we worked with letters signed with the right hand, the glove is worn on the user's right hand with the lights on the palm side. On the back of the hand is a coin-cell battery holder that powers our LEDs with a CR2032, 3-volt, lithium, coin-cell battery. As shown in Figure 1, the thumb and pinkie have matching blue lights, the palm and ring finger have matching red lights, the index finger has a yellow light, and the middle finger has a green light. The LED color corresponding to each point of interest was chosen arbitrarily, however the placement of the duplicated colors (red and blue) was a specific design choice. These elements are attached to the glove and the LEDs pull current from the battery in parallel through conductive thread. The positive terminal of each LED is wrapped in 2-3 loops of conductive thread. This thread then runs in mid-size running stitches to the positive terminal of the battery holder, which also gets looped around 2-3 times. Then, we connected the negative terminals using the same process. Figure 2 shows how our LEDs are connected to the battery without crossing positive and negative threads, as well as reducing potential for contact when adjacent fingers are touched together.



Figure 2: Connections on LED glove (positive is red, negative is gray)

At this point, the battery will power the LEDs, however, we took a few extra steps to further improve the glove. To start, threads are exposed and can easily touch other threads when moving through signs. In order to prevent this and protect the thread from fraying, we insulated the stitches with fabric paint and allowed it to dry. In addition, since the terminals of the LEDs are especially prone to bumping into each other, we used hot glue to protect the LED terminals and the thread looped around them. In addition, we needed to ensure the LEDs could be seen clearly by the camera. When LEDs are too bright, the center looks white when perceived by the camera, creating a ring of color around a white center rather than a dot of color, so we diffused our yellow LED with a strip of paper secured over the light. Finally, the green LED was dimmer

than the other colors, making it appear smaller. To combat this, a dot hot glue (smudged to increase its opacity) covers the light, diffusing it so it appears larger to the camera. The glove in its final form is shown in Figure 1.

While we enjoyed improving our sewing skills, we faced some challenges designing and creating the glove. To start, the first glove prototype was sewn with very large running stitches, which created faulty connections and often didn't work. In addition, we didn't realize how frequently threads would touch when signing, so we had to go back and add more insulation multiple times. One shocking discovery we made is that, possibly due to sweat, wearing the glove for a long time often caused a slight buzzing sensation in the hand since the threads were uninsulated on the inside. Learning all of these lessons allowed us to create the best version of our glove possible, which is both comfortable for the user and functional for our system.

8:0] thun dentificatio [8:0] inc done Gesture LED Position Finger Recognition [8:0] ring Finder Extraction FSM [8:0] pin [8:0] pa dentificatio XVGA 4:0] letter [11:0] pixel_out ASCII , RGB-to-HSV **BRAM** Converter [11:0] RGB Text LED Position [11:0] RGB Display Frame Buffer Camera 0] text rom a BRAM [11:0] RGB [11:0] RGB

4 System Overview and Block Diagram

Figure 3: Top Level Module

The top level module of our design (depicted by the gray background in Figure 3) utilizes the Frame Buffer, LED Position BRAM, RGB to HSV converter, LED Position Extraction, Finger Finder, Gesture Recognition and J and Z Identification, Text Display, ASCII BRAM, and XVGA modules to generate VGA outputs given a camera input. Camera data is first passed to the LED Position BRAM to accumulate a full frame of data for processing. Once a frame is assembled, the RGB to HSV Converter takes in the RGB pixels in the frame and outputs pixel values in the HSV format. HSV pixels are then passed to the LED Position Extraction module where chroma keying is used to identify the positions of up to 6 of the glove-mounted LEDs. These positions and corresponding LED colors are passed to the Finger Finder module where the positions are assigned to the points of interest. The positions of the points of interest are passed into the gesture recognition module, which outputs the letter recognized from the LED positions either through static recognition or through interaction with J and Z Identification modules. The Text Display module uses this letter input as the input to the ASCII BRAM, which returns the appropriate pixel for the current pixel count. The Text Display module then outputs this pixel data and finally, the XVGA module formats this pixel data into a proper VGA output.

5 Modules

5.1 Camera

The camera input is created through a few hardware and software pieces, being an OV7670 camera, an ESP8266 microcontroller, the microcontroller Camera Control module, the FPGA Camera Read module, and the Frame Buffer. Starter code for these modules was provided, however the microcontroller Camera Control module needed brightness and saturation adjustments in order to suit the needs of the project. Once the camera data is received from the camera hardware, it is written to two separate BRAM modules. One of these is the aforementioned Frame Buffer which outputs raw camera data to the monitor; the other is an identical BRAM which provides data for the recognition pipeline. Despite containing the same data, these two BRAMs are accessed at different rates, therefore requiring two independent modules.

5.2 RGB to HSV Converter

The input from the camera is an RGB value for each pixel, which means each pixel has a separate value for the red, green, and blue intensity of the pixel. The converter, written by Kevin Zheng in 2010 (shoutout Kevin), produces an HSV representation of the pixel, giving a value (number) for the hue, saturation, and value (brightness). This hue value will be important in chroma keying. In 2010, when Kevin Zheng wrote this converter, the IP for a divider was slightly different than what Xilinx generates for users today, so we made slight alterations to fit the newer IP into the module. However, the current IP still has a 20 cycle latency, where the converter requires an 18 cycle latency. We were able to circumvent this issue, as the last two cycles of latency were a result of calculating the remainder, which is of no importance to us and can, as a result, be ignored.

5.3 LED Position Extraction (Marc)

The first of our two major modules, the LED Position Extraction module, takes in 8-bit hue, saturation, and value integers which represent a single RGB565 pixel. With this pixel, and the 76,799 (320x240) pixels that will follow, we need to determine where in the camera frame the (up to) 6 LEDs are positioned.

5.3.a Implementation Details



Figure 4: Grid of bins

This entire module is centered around the idea of a "bin" which we defined as a 15x10 (row-column) pixel area. These bins are arranged in a 16x32 grid, as shown in Figure 4, within the camera frame, such that all pixels within the camera frame fall into one of these 512 bins. In order to access each bin, all of which are represented by four dual-port RAMs (one for each LED color), we require a way to update the input address "automatically". We accomplish this through the Cell Determiner module, which uses the current x and y positions of a pixel and internal counters to output a BRAM address. With this groundwork in place, we can now describe the position extraction process.

To begin, the module remains idle until the start signal is asserted. Upon assertion, the HSV values currently on the input wire are passed to a simple combinational module which checks to see which color range the hue value resides in. Depending on this output, one of four temporary color registers is incremented to indicate that a pixel of the register's color was recognized. This process is repeated for 7 more cycles until the edge of the first bin is reached, at which point we sum the value (initially 0) at the current BRAM address with the temporary color

register and replace the value in the BRAM; this occurs for each of the 4 colors in parallel. This is repeated for the following bins in the current row until the last pixel in the row is reached. If all 16 pixel rows have been counted for the current row of bins, the bin address is set to the first column of the next bin row; otherwise, the bin address is set back to the first column of the current bin row so that all pixel rows get counted for the current row of bins. This process continues until all 76,800 pixels in a single frame have been accounted for in the bin sums. Following the processing of the last pixel, the Cell Determiner module outputs a done signal to indicate that the LED Position Extraction module can proceed.

On the assertion of this signal, the module begins iterating over all 512 addresses of the completed bins. After a 2 cycle delay, to account for the BRAM read latency, the value from each address (the number of pixels of a color in a bin) is checked to see if it is above a specified threshold; if it is, the x and y positions of the bin, prepended with the color that was above the threshold, is set on one of the 6 possible indices in the output array. For simplicity sake, the thresholds are checked in the predetermined order: red, blue, green, then yellow. Once all 6 of the output positions have been assigned an output, or all the bins have been checked (whichever occurs first), the module asserts "done" and begins the resetting process. This process takes 512 cycles since the values at all the addresses of the bin BRAM must be set back to 0 in preparation of the next frame.

The entire process takes roughly 77,826 cycles at 65MHz, though the exact timing is not important for our purposes, so long as the module is complete before a new frame is available in the frame buffer. This happens to be the case for our system, however, even if this were not the case and we were limited to only processing, for example, every other frame, there would be no visible difference for the user.

5.2.b Color Recognition (or lack thereof)

This module, of course, did not not come without its own challenges in implementation. The most obvious example of this is the difficulty of color recognition. Starting with the camera sensors, we immediately lose some color data due to suboptimal camera initialization settings and to the oversaturation of the sensors by the LEDs (though this was slightly alleviated by diffusing the problematic colors). Additionally, we lose color data in the, arguably necessary, conversion of the RGB565 values to an 8-bit hue. All of this is on top of the disadvantageous similarities in wavelength of some of the colors (e.g. yellow and red). Even with all these factors, half of the colors, specifically blue and green, remained reliably recognizable. Red and yellow, however, started off as very unreliably differentiable.

Getting these colors properly differentiated required significant experimentation with upper and lower limits for each of the hue ranges that correspond to the four colors. A value threshold was also needed so we could filter out background colors that would be recognized unintentionally. However, we found even with these changes, there was still large enough variance in the appearance of the LEDs (due to subtle lighting changes and different brightnesses) that we required one more threshold in order to reach an acceptable level of reliability: the count of pixels of one color in each bin. We were very surprised to find that this threshold worked best when set to a value less than 20 pixels (i.e. ~13% of a bin) for all four colors.

All this experimenting was made possible through the use of the Virtual Input/Output IP in Vivado which allowed us to shift these limits as the module displayed an output to the monitor. This IP was also used for determining the pixel number threshold for the classification of bins. Given its usefulness throughout our project, we highly recommend using this IP for debugging a hardware implementation, as long as the trigger functionality of the ILA IP is not explicitly required.



5.3.c Grid Size Considerations

Figure 5: Extreme double counting of a single LED

The other main challenge in getting the LED Position Extraction module to function properly was determining the size of the grid (and therefore the size of the bins). Fundamentally, we were not able to make the bin area very small, since we would run the risk of counting a single LED in more than one bin, as shown in Figure 5 (this issue affects letters like "B", "C", and "O" the most, since there are only 6 position outputs available, meaning a double count causes another LED to be ignored). Conversely, we were not able to make the bins too large, since we would run the risk of grouping two LEDs into one bin which would limit us from

recognizing signs with adjacent fingers. Additionally, if the bins are too large, we would lose too much position information for the Gesture Recognition module to be effective.

Since the issue of double counting would likely limit our lower bound of bin area too much, we opted to add logic which would explicitly avoid this. We do this by checking if any of the four adjacent bins to the bin in question was previously recognized as the same color as the bin in question; if this is the case, we ignore the current bin and proceed normally. This greatly reduces the amount of double counting, though if someone were to replicate this project, we would recommend increasing the radius of bins this checks, as there is still room to increase this without risking counting two legitimately different LEDs as one.

Luckily enough, with the aforementioned fix in place, our first guess of bin size worked well enough that we didn't see any reason to try other bin sizes. Given this, we believe there is a decently sized range of bin areas that would result in the same, or even better, functionality. One aspect we did not experiment with was the shape of individual bins, though we believe a recreation of this project could benefit from a more square shaped bin that more accurately captures the circular appearance of the LEDs.

5.4 Finger Finder (Deb)

Between our two main modules is the Finger Finder module. The purpose of this module is to take the LED locations in a frame and assign them to their corresponding points of interest, being the thumb, palm, and the index, middle, ring, and pinkie fingers. This module takes the LED Position Extraction output, consisting of six, 12-bit values. The three most significant bits represent the color of the LED at the identified location (a value from one to four), the next five bits represent the bin number in the x-direction, and the last four bits represent the bin number in the x-direction, and the last four bits represent the bin number in the y-direction. Since not all six LEDs will be present in every frame, the array will have valid color entries for however many LEDs are identified, and the remaining entries in the array will be 0, meaning the array contains no more valid positions.

With our current design, this module requires certain assumptions about hand positions in the ASL alphabet. These assumptions are necessary in order to assign LEDs of our repeated colors, but are effective and mostly unproblematic for gesture recognition. The first assumption is that, from the user's perspective, the thumb is always to the left of the pinkie. This is mirrored from the camera's perspective, giving the thumb a larger bin value than the pinkie in the x-direction. This assumption allows us to separate the thumb from the pinkie when both appear in the frame (two blue lights). Similarly, we assume the ring finger is always above the light on the palm, giving the ring finger a smaller bin value in the y-direction. This allows us to separate the two red LEDs as the ring finger and palm. These two assumptions are consistent in the recognition of all letters. We must also consider the case where only one light of a duplicated

color is present. For red lights, the palm light is present in every letter we recognize and the ring finger is not, so if there is just one red LED we recognize it as the palm. For blue lights, many signs include the thumb and not the pinkie, but the pinkie does not appear without the thumb. As a result, a single blue light is recognized as the thumb. This is the only imperfect assumption thus far, due to the movement involved when signing "J". In this sign, the rotation of the hand covers the thumb light before the pinkie light. As a result, only the pinkie is visible for part of the motion. Anticipating this allowed us to switch our finger of interest to the thumb when looking for the pinkie in the J Identification module.

The module starts by waiting until the LED Position Extraction module signals it has finished processing the current frame, meaning the current positions are valid. Validity is only guaranteed for one cycle, so the array must be saved to continue using these values in subsequent clock cycles. When the start signal is asserted, the array is saved, the cycle count is set to 0, and the position of each finger is set to 0. Over the next six clock cycles, we use the cycle count to index into the array. On any of these cycles, if the color is 0 we skip the remainder of the array since there are no more valid positions. If a yellow or green color is found on any cycle, we assign the position in this entry to the index finger or middle finger, respectively. The first cycle is the simplest for the blue and red lights, since the assumption that a single light in these colors belongs to the thumb and palm means we assign these points at the first blue or red light in the array. In the next five cycles, if the entry is a blue light, we first check if we have stored a location as the thumb on a previous clock cycle. If we have not, we store this location as the thumb. If we have, we employ the assumption that the thumb is on the user's left and will store the position with the higher bin value in the thumb, and the position with the lower bin value in the pinkie. Similarly with a second red light, the higher bin value will be the palm and the lower bin value will be the ring finger.

This module asserts a done signal in two cases. Any time the color is 0, done is asserted to signal the locations are finished being assigned. In addition, when we have processed all the positions in the array, we assert the done signal. The output of the module is one, 9-bit variable for each LED on the hand, labeled as thumb, index, middle, ring, pinkie, and palm, containing the position of the bin where that point of interest is. These values are a valid position for any point of interest in the frame, and 0 for any not showing.

5.5 Gesture Recognition (Deb)

The Gesture Recognition module is the other main module in our system. The inputs correspond to the position of each point of interest from the Finger Finder module. The output of the module is a 5 bit representation of the letter (space = 0, "A" = 1, ..., "Z" = 26) and a done signal when a letter is to be displayed. The module is mainly a static recognition state machine to

identify the 24 static letters, with instances of the dynamic recognition modules to identify "J" and "Z".



Figure 6: Lights visible to system in ASL alphabet

5.5.a Static Letter Recognition

The static recognition state machine begins in the idle state. When the Finger Finder module asserts the done signal, the state machine moves to the static recognition state to determine the letter created with the lights shown in the newly valid locations. The static recognition state is broken into the subsets of letters signed with the same lights showing. The lights showing in each letter are shown in Figure 6. For example, one of these subsets consists of the letters "A", "E", "N", "S", and "T", which are all signed with the thumb and the palm showing and all other points hidden. If a frame is not showing lights corresponding to one of the

eight subsets of letters, the state machine returns to the idle state, which will cause no change in the display.

When a subset is chosen, the given positions are checked for identifying features of each letter in the subset. For example, "A" creates the largest distance in the x-direction between the thumb and the palm. If the x-distance between the current positions is above the predetermined threshold value, the letter is recognized as "A". The letter variable is set to 1, the number corresponding to "A", and the state machine transitions to the debounce state.

The debounce state is how the user is saved from small variations or single frame mistakes causing changes in the displayed letter. This state uses a saved copy of the previous letter to see if our letter has stayed consistent. If the letter matches the previous letter, a counter is incremented. If not, the counter is reset. If the letter matches for four frames, the done signal is asserted so the display module knows the letter is ready to be shown on the monitor.

The recognition of letters once a subset is identified is the most difficult part of the static recognition state machine, and is the feature that required the most testing and fine tuning. Subsets containing one letter, such as "M" or "F", are the simplest, since it is enough to recognize the correct lights to recognize the letter. However, having more letters within a subset, requires more distinctive features in order to separate the letters. For example, in the case of "A", "E", and "S", the palm and thumb are the only lights present in the frame and the thumb has the same bin height for all three signs. So the signs must be separated by the thumb's position in the x-direction relative to the palm. Each sign requires its own distance threshold between the two points, so that space must be partitioned into three sections to recognize all three letters. One challenge in writing this module is narrowing those threshold distances to catch all signs of a letter without including any other signs.

5.5.b Dynamic Letter Recognition

Dynamic characters "J" and "Z" cannot be recognized in the static recognition state machine because the locations of the points of interest must be processed over the course of many frames in order to recognize the letter. As such, two separate state machines handle the recognition of "J" and "Z" respectively. These modules have instances in the gesture recognition module and work in parallel with the static recognition. This is so a user can begin to sign a dynamic character, but not complete the character and move onto another letter without the dynamic recognition getting stuck due to the partially signed letter. This way, the new letter can be detected by the static recognition state machine and cause the dynamic module to exit.

The static recognition state machine interacts with the dynamic modules by providing the positions of relevant points of interest and a start signal for the module to begin recognition,

while the dynamic modules signal when their dynamic character has been identified. The trigger for the "J" and "Z" modules are set when the static recognition state machine outputs a done signal with the letters "I" and "D" respectively. This is because the dynamic letters start with these hand positions before the movement begins. As previously stated, this does not disrupt the static recognition state machine, as it will continue through the states while the module works. Then, when a dynamic identification module is complete, it signals its letter has been recognized and should now be displayed. This is the only portion of dynamic recognition that is blocking, because the letter being output is set to the recognized dynamic character for 2 seconds before the static recognition state machine can process inputs again. This allows the user time to lock in the letter, since there isn't a position to hold like in static recognition.

5.5.c False Recognition

Since the Gesture Recognition module is what decides the letter output on the monitor, this is where mistakes arise. Incorrect letters stem from two main types of errors: user correctable errors and position extraction error.

User correctable errors will cause the state machine to output a letter from the correct subset of letters, though the exact letter will be incorrect. This is because all the lights have been correctly identified as the necessary points of interest, but the user's sign positioning does not meet the requirements the module has set for that letter. This can be due to the user tilting their hand while making the sign or altering the relative positions so they appear in range of another letter. One example of this is "T" and "N", which are in the same subset of letters because they show only the palm and thumb and have the same relative distance between the thumb and palm in the y-direction. However, the thumb's x position for either letter is just on opposite sides of the palm light, with the "T" having the thumb to the user's left of the palm and the "N" on the right. Because the number of bins separating the two is not very large, if the user tilts their hand to their right while giving the sign for "T", the state machine will recognize the sign as "N" since the thumb will cross to the other side of the palm and into the range for "N" recognition. This type of error is easy to correct, especially if the user is familiar with the system. In this case, the user will see the incorrect letter on the display, look at their light positions, and realize their hand is not straight up and down. Once the user reorients their sign, the display will show the intended letter. This error becomes less common as a user gets more familiar with the system and gets used to making signs the system recognizes as intended. If the system is used by just one user, the thresholds could even be adjusted to recognize their specific style of signs with little trouble.



Figure 7: Misrecognition of "C"

The second kind of error that will cause an incorrect letter to appear on the display results is a fault in the recognition of LEDs located by the Position Extraction module. This can occur when an LED is unrecognized, recognized as the wrong color, or double counted. This is a more significant issue for recognizing the letter, since this will cause the static recognition state to select the incorrect subset of letters to choose from. As described in Section 5.3, the yellow light, which represents the index finger, is the most difficult to recognize. This lack of recognition causes signs produced with the index finger showing to register as a sign with all the same lights, but without the index finger showing. For example, "B", "C", and "O" are signed with all six lights visible and "F" is signed with all but the index finger visible. This means that if the user gives the sign for "B", "C", or "O" and their yellow light is too dim, their hand is too far from the camera, or their angle is such that the yellow LED is not recognized by the LED Position Extraction module (which the user will know due to the absence of the yellow crosshair on the grid), the module will produce the letter "F" as the output. This is shown in Figure 7, in which "C" is displayed as "F" due to the missed recognition of the index finger. Similar to the previous error discussed, an experienced user will see this issue and know to replace their battery or adjust their hand's position relative to the camera until the light is more consistently recognized. However, this issue is more difficult for the user to know how to fix, since they might not have a sense for the best distance or angle for recognition. It can be difficult to adjust these thresholds to allow yellow to be recognized more easily, since its proximity to red and the difficulty distinguishing the boundary between the two can lead to a red LED registering as both a red and a yellow LED if the threshold for yellow is lowered too much, which leads to the same issues.

In a future iteration of the project, user correctable error is likely to remain due to the limitations of recognizing signs with a camera and lights. However, position extraction error could be mitigated in a future iteration by having more strict recognition conditions in order to avoid some recognition within an incorrect subset of letters. In the example letters given above, "C" and "O" are signed with middle and ring finger positions that could be distinguishable from the ones present in "F" by checking the palm to finger distance in the y-direction. As a result, a more thorough recognition process would only output "F" in the case that the positions of the index and middle finger don't resemble those of "C" and "O". However, even with these additional checks, the "B" to "F" error will likely still occur, since the other finger positions are not easily differentiable. Therefore, it is possible to avoid some incorrect recognition, but even with thorough analysis of misidentified letters, it does not appear to be absolutely avoidable without creating another distinguishable difference between the letters (such as shifting to a wider spread in the middle and ring fingers when signing "F" to create greater x-direction separation than in "B").

5.6 J and Z Identification (Marc)

Generally, the structures of both the "J" and "Z" identification modules are similar with only minor differences between the actual recognition of the letters. As such, we will describe only one module in-depth and simply relate it to the other module after.

5.6.a Implementation

Using "J" as an example, the module begins with the assertion of "I", as mentioned in Section 5.5.b. On the same cycle, current relevant finger positions (thumb and pinkie positions for "J") are stored in registers and the state is moved from idle to the activated state. The module then waits until the real position of the pinkie (passed into the module as the thumb due to previous assumptions in the pipeline) is a specified distance below the initial thumb position. This detects the downward sweep that initially occurs when signing a "J". Once this is detected, the state machine moves forward and similar logic is present to detect the leftward sweep at the end of the sign. Throughout this process, the only time the FSM resets to the idle state is when a different letter is output by the static recognition state machine (for reasons explained in Section 5.6.b). Once both these motions are detected, a done signal is asserted for one cycle and the FSM resets to idle. The only differences for the "Z" identification module are the storing of the index finger initially and different required motions (left-down/right-left).

5.6.b Accidental Recognition

Our initial goal when designing these modules was to require the user to follow a defined, though not strict, path to sign each letter, with large enough deviations from the path resulting in a reset of the FSM. This would make it very difficult to accidentally sign one of the letters in question. We quickly realized, though, that this wouldn't be possible due to the assumptions that we were required to make previously. Specifically, the way our module determines thumb and pinkie positions caused there to be unpredictable patterns in the positions given as the thumb and pinkie. This led to frequent, accidental invalidation of valid signs. In order to avoid this, we settled upon the current system where the only way to invalidate the sign is by the static recognized. This, of course, causes "J" detection to be much less robust against false positives ("Z" is less affected due to the extra motions required). Since the error rate was somewhat low, we simply accepted this as a necessary consequence, however we believe it's possible to slightly reduce this false positive rate by adding more states/required movements to complete the sign.

5.7 Text Display (Marc)



Figure 8: Character dimensions as represented in BRAM

Our final module of the pipeline, the Text Display module, is relatively simple compared to the previous ones. At its core, the Text Display module is a simple finite state machine with 64 states, one for each space a letter can occupy, resulting in a maximum character length of 64. The

slight complexity of this module comes from the way in which the ASCII characters are displayed over VGA from a single-port ROM. Each character "drawn" by the COE file the ROM is initialized with is 16-bits tall by 8-bits wide, resulting in a depth of 2048 bits and a width of 8 bits. The module uses a specified start address along with an internal offset counter to access character data 8 bits at a time. Once read, these bits are stored in a temporary register where the bit at the 7th index is passed to the pixel_out output to be sent along the VGA connection to the monitor. Every cycle, this temporary register is shifted to the left by 1 to access the next appropriate pixel for displaying. This process is repeated for all 64 available character indices 16 times to capture all the rows of each character and once finished, the module resets all counters to prepare for the next frame.

6 Conclusion

As a team, we achieved all the goals we set out to accomplish and are satisfied with the final system. We are proud to have designed and created a system that recognizes all 26 letters of the ASL alphabet, including the two dynamic letters we initially categorized as a stretch goal, all the while adhering to our initial goal of staying as faithful to true ASL as much as possible. In addition, we feel the current system serves as a strong base upon which further functionality could be implemented, given a longer timeline. Were we to return to this project at a later date, our top priority would be implementing functionality of the system for left-handed users in addition to the currently supported right-handed users. Our vision for this would be to automatically detect which hand the user is signing with and use the appropriate internal logic to identify signs as before. At an overall system perspective, one change we would make is in the LED arrangement; knowing the current accuracy of LED position extraction, we would use a more reliably recognized LED, like green, as a duplicated color instead of the red we currently use. An ultimate goal for us would be to reach a high enough level of reliability in this base system that would allow us to pursue recognition of entire ASL words and phrases.

7 Appendix

Appendix

Top Level

1

```
2
   `timescale 1ns / 1ps
3
4 module top_level(
5
      input clk_100mhz,
6
      input[15:0] sw,
7
      input btnc, btnu, btnl, btnr, btnd,
8
      input [7:0] ja, //pixel data from camera
9
      input [2:0] jb, //other data from camera (including clock return)
10
      output jbclk, //clock FPGA drives the camera with
      input [2:0] jd,
11
12
      output jdclk,
13
      output[3:0] vga_r,
14
      output[3:0] vga_b,
15
      output[3:0] vga_g,
16
      output vga_hs,
17
      output vga_vs,
18
      output led16_b, led16_g, led16_r,
19
      output led17_b, led17_g, led17_r,
      output[15:0] led,
20
21
      output ca, cb, cc, cd, ce, cf, cg, dp, // segments a-g, dp
22
      output[7:0] an // Display location 0-7
23
      );
24
      parameter CAM_WIDTH = 320; //width of each camera frame in pixels
      parameter CAM_HEIGHT = 240; //height of each camera frame in pixels
25
26
27
      localparam NO_COLOR = 0;
28
      localparam RED = 1;
29
      localparam BLUE = 2;
30
      localparam GREEN = 3;
31
      localparam ORANGE = 4;
32
33
           logic clk_65mhz;
34
       // create 65mhz system clock, happens to match 1024 x 768 XVGA timing
35
       clk_wiz_0 clkdivider(.clk_in1(clk_100mhz), .clk_out1(clk_65mhz));
36
37
       wire [6:0] segments;
38
39
       assign dp = 1'b1; // turn off the period
40
41
       assign led = sw;
                                                // turn leds on
42
43
       assign led16_r = btnl;
                                                // left button -> red led
                                                // center button -> green led
44
       assign led16_g = btnc;
45
       assign led16_b = btnr;
                                                // right button -> blue led
46
       assign led17_r = btnl;
47
       assign led17_g = btnc;
48
       assign led17_b = btnr;
49
50
       wire [10:0] hcount; // pixel on current line
51
                              // line number
       wire [9:0] vcount;
```

```
52
        wire hsync, vsync, blank;
53
        wire [11:0] pixel;
54
        reg [11:0] rgb;
55
        xvga xvga1(.vclock_in(clk_65mhz),.hcount_out(hcount),.vcount_out(vcount),
56
               .hsync_out(hsync),.vsync_out(vsync),.blank_out(blank));
57
58
59
        // btnc button is user reset
60
        wire reset;
61
        debounce db1(.reset_in(reset),.clock_in(clk_65mhz),.noisy_in(btnc),.clean_out(
            reset));
62
63
        //sw[15] is used to confirm a letter
64
        logic text_display_trigger;
65
        debounce db5(.reset_in(reset),.clock_in(clk_65mhz),.noisy_in(sw[15]),.clean_out(
            text_display_trigger));
66
67
        //sw[14] is used to delete a letter
68
        logic text_display_trigger_delete;
69
        debounce db6(.reset_in(reset),.clock_in(clk_65mhz),.noisy_in(sw[14]),.clean_out(
            text_display_trigger_delete));
70
71
        logic xclk;
72
        logic[1:0] xclk_count;
73
74
        logic pclk_buff, pclk_in;
75
        logic vsync_buff, vsync_in;
76
        logic href_buff, href_in;
77
        logic[7:0] pixel_buff, pixel_in;
78
79
        logic [11:0] cam;
                                             //VGA output pixel data received from the
            camera
80
        logic [11:0] frame_buff_out;
                                             //pixel data from the frame buffer
81
        logic [15:0] output_pixels;
                                             //pixel data from camera
82
        logic [15:0] old_output_pixels;
83
        logic [12:0] processed_pixels;
                                             //camera pixel data with truncated r, g, and b
             values
84
        logic valid_pixel;
85
        logic frame_done_out;
                                             //true when the frame buffer contains the full
             previous frame
86
87
                                             //frame buffer input address
        logic [16:0] pixel_addr_in;
                                             //frame buffer output address
88
        logic [16:0] pixel_addr_out;
89
90
        logic [11:0] converter_out;
                                             //input to the RGB-to-HSV converter
91
92
        //RGB-to-HSV values
93
        logic [7:0] hue;
 94
        logic [7:0] saturation;
95
        logic [7:0] value;
 96
97
        logic extraction_start;
                                             //start signal for the position extraction
            module
98
                                             //current pixel x position
        logic [8:0] extraction_x;
99
        logic [7:0] extraction_y;
                                             //current pixel y position
100
                                             //true when position extraction module is
        logic counting;
            working
101
        logic [4:0] converter_delay;
                                             //delay which accounts for BRAM read and RGB-
            to-HSV delay
```

```
102
                                              //address for pixel input to RGB-to-HSV
        logic [16:0] pixel_addr_to_hsv;
            converter
103
        logic [11:0] led_positions [5:0]; //length 6 array of 12-bit wide led positions
104
105
        //number of pixels required to assign a bin a color
106
        logic [7:0] red_threshold;
107
        logic [7:0] blue_threshold;
108
        logic [7:0] green_threshold;
109
        logic [7:0] orange_threshold;
110
111
        //lower and upper hue bounds for each color
        logic [7:0] red_lower;
112
113
        logic [7:0] red_upper;
114
        logic [7:0] blue_lower;
        logic [7:0] blue_upper;
115
116
        logic [7:0] green_lower;
117
        logic [7:0] green_upper;
118
        logic [7:0] orange_lower;
119
        logic [7:0] orange_upper;
120
121
        logic [7:0] value_threshold;
                                             //minimum value to count a pixel as a color
122
123
        //led positions from last position extraction cycle; used to create colored
            crosshairs
124
        logic [11:0] last_pos0;
        logic [11:0] last_pos1;
125
126
        logic [11:0] last_pos2;
127
        logic [11:0] last_pos3;
128
        logic [11:0] last_pos4;
129
        logic [11:0] last_pos5;
130
131
        //finger positions from finger finder module
132
        logic [8:0] thumb_pos;
133
        logic [8:0] index_pos;
134
        logic [8:0] middle_pos;
135
        logic [8:0] ring_pos;
136
        logic [8:0] pinkie_pos;
137
        logic [8:0] palm_pos;
138
139
        logic gesture_done;
                                              //true when gesture recognition fsm has found
            a letter
140
        logic [4:0] letter;
                                              //letter from GR-fsm
141
142
        logic [11:0] text_pixel_out;
                                              //pixel used for text display
143
        logic text_display_confirm_letter;
144
        logic text_display_delete_letter;
145
146
        assign xclk = (xclk_count >2'b01);
147
        assign jbclk = xclk;
148
        assign jdclk = xclk;
149
150
151
        //BRAM used for storing and displaying camera data to the monitor
152
        blk_mem_gen_0 jojos_bram(.addra(pixel_addr_in),
153
                                   .clka(pclk_in),
154
                                   .dina(processed_pixels),
155
                                   .wea(valid_pixel),
156
                                   .addrb(pixel_addr_out),
157
                                   .clkb(clk_65mhz),
```

```
158
                                    .doutb(frame buff out));
159
160
         //BRAM used for inputting camera data to GR-fsm pipeline
161
         blk_mem_gen_0 led_position_bram(.addra(pixel_addr_in),
162
                                           .clka(pclk_in),
163
                                           .dina(processed_pixels),
164
                                           .wea(valid_pixel),
165
                                           .addrb(pixel_addr_to_hsv),
166
                                           .clkb(clk_65mhz),
167
                                           .doutb(converter_out));
168
169
         //instantiates the RGB-to-HSV converter
170
         rgb2hsv my_converter (.clock(clk_65mhz),
171
                                .reset(reset),
172
                                .r({converter_out[11:8], 4'b0}),
173
                                .g({converter_out[7:4], 4'b0}),
174
                                .b({converter_out[3:0], 4'b0}),
175
                                .h(hue),
176
                                .s(saturation),
177
                                .v(value));
178
179
         //Virtual input/output IP used for setting various thresholds for the led position
              extraction module
180
         vio_0 threshold_picker (
181
           .clk(clk_65mhz),
182
           .probe_in0(led_positions[0]),
183
           .probe_in1(led_positions[1]),
184
           .probe_in2(led_positions[2]),
185
           .probe_in3(led_positions[3]),
186
           .probe_in4(led_positions[4]),
187
           .probe_in5(led_positions[5]),
188
           .probe_out0(red_threshold),
189
           .probe_out1(blue_threshold),
190
           .probe_out2(green_threshold),
191
           .probe_out3(orange_threshold),
192
           .probe_out4(red_lower),
193
           .probe_out5(red_upper),
194
           .probe_out6(blue_lower),
195
           .probe_out7(blue_upper),
196
           .probe_out8(green_lower),
197
           .probe_out9(green_upper),
198
           .probe_out10(orange_lower),
199
           .probe_out11(orange_upper),
200
           .probe_out12(value_threshold)
201
         );
202
203
         //instantiates the position extraction module
204
         led_position_extraction my_extractor (.clock(clk_65mhz),
205
                                                  .reset(reset),
206
                                                  .start(extraction_start),
207
                                                  .h(hue),
208
                                                  .s(saturation),
209
                                                  .v(value),
210
                                                  .x(extraction_x),
211
                                                  .y(extraction_y),
212
                                                  .red_threshold(red_threshold),
213
                                                  .blue_threshold(blue_threshold),
214
                                                  .green_threshold(green_threshold),
215
                                                  .orange_threshold(orange_threshold),
```

	.red_lower(red_lower),
	.red_upper(red_upper),
	.blue lower(blue lower),
	blue upper(blue upper).
	areen lower(areen lower).
	groon upper (groon upper)
	.green_upper(green_upper),
	.orange_rower(orange_rower),
	.orange_upper(orange_upper),
	.value_threshold(value_threshold),
	.led_positions(led_positions),
	.done(extraction_done),
	<pre>.ready(extraction_ready));</pre>
//instantiates the finger	r_finder module; is assigns each position to a specific
finger_finder my_finder /	(.clock(clk_65mhz),
5 _ 1 _	.reset(reset),
	.led positions(led positions).
	.start(extraction done).
	done(finder done).
	thumb (thumb nos).
	index (index nos)
	middle(middle pos)
	.mruure(mruure_pos),
	.ring(ring_pos),
	.pinkie(pinkie_pos),
	.palm(palm_pos));
//instantiates gesture re	ecognition fsm
gesture recognition fsm m	ny gesture (.clock(clk 65mhz).
<u> </u>	reset (reset).
	start (finder done)
	dono (gosturo dono)
	. dolle (gescure_dolle),
	.tnumb(tnumb_pos),
	.index(index_pos),
	.middle(middle_pos),
	.ring(ring_pos),
	.pinkie(pinkie_pos),
	.palm(palm_pos),
	.letter(letter));
//instantiates text disn'	lay. used to display the letters that are signed
text display my toxt diar	ally, about to aloping the retters that are sighed
cere_urspray my_cere_ursp	reset (reset)
	<pre>.reset(reset), heavent in (heavent)</pre>
	.ncount_in(ncount),
	.vcount_in(vcount),
	.letter(letter),
	.done(gesture_done),
	.confirm_letter(text_display_confirm_letter),
	.delete_letter(text_display_delete_letter),
	.pixel_out(text_pixel_out));
assign {cg, cf, ce, cd, c	<pre>cc, cb, ca} = segments[6:0];</pre>
<pre>//display_8hex display(</pre>	<pre>clk_in(clk_65mhz),.data_in({thumb_pos[3:0], index_pos</pre>
[3:0], middle_pos[3:0], ring_pos[3:0], pinkie_pos[3:0], palm_pos[3:0], 8'b0}),
.seg_out(segments), .	<pre>strobe_out(an));</pre>
<pre>display_8hex display(.clk strobe_out(an));</pre>	<pre>c_in(clk_65mhz),.data_in(letter), .seg_out(segments), .</pre>

270 always_ff @(posedge pclk_in)begin

```
271
              //handles addressing for camera data input to BRAM
272
              if (frame_done_out) begin
273
                  pixel_addr_in <= 17'b0;</pre>
274
             end else if (valid_pixel)begin
275
                  pixel_addr_in <= pixel_addr_in +1;</pre>
276
             end
277
         end
278
279
         //states used to avoid double-counting letter confirmations/deletions
280
         logic text_display_state;
281
         logic text_display_state_delete;
282
283
         always_ff @(posedge clk_65mhz) begin
284
             pclk_buff <= jb[0];//WAS JB</pre>
285
             vsync_buff <= jb[1]; //WAS JB</pre>
286
             href_buff <= jb[2]; //WAS JB</pre>
287
             pixel_buff <= ja;</pre>
288
             pclk_in <= pclk_buff;</pre>
289
             vsync_in <= vsync_buff;</pre>
290
             href_in <= href_buff;</pre>
291
             pixel_in <= pixel_buff;</pre>
292
             old_output_pixels <= output_pixels;</pre>
293
             xclk_count <= xclk_count + 2'b01;</pre>
294
             processed_pixels = {output_pixels[15:12],output_pixels[10:7],output_pixels
                  [4:1]};
295
296
              //handles confirmation double counting
297
              if (text_display_state == 0 && text_display_trigger) begin
298
                  text_display_confirm_letter <= 1;</pre>
299
                  text_display_state <= 1;</pre>
300
             end else if (text_display_state == 1) begin
301
                  text_display_confirm_letter <= 0;</pre>
302
                  if (text_display_trigger == 0) text_display_state <= 0;</pre>
303
             end
304
305
             //handles deletion double counting
306
             if (text_display_state_delete == 0 && text_display_trigger_delete) begin
307
                  text_display_delete_letter <= 1;</pre>
308
                  text_display_state_delete <= 1;</pre>
309
             end else if (text_display_state_delete == 1) begin
310
                  text_display_delete_letter <= 0;</pre>
311
                  if (text_display_trigger_delete == 0) text_display_state_delete <= 0;
312
             end
313
314
             if (frame_done_out && extraction_ready) begin
                                                                   //when position extraction is
                  ready and a new frame is available
315
                  counting <= 1;
316
                  converter_delay <= 0;
317
                  pixel_addr_to_hsv <= 0;</pre>
318
                  extraction_x <= 0;</pre>
319
                  extraction_y <= 0;</pre>
320
              end else if (counting) begin
321
                  if (converter_delay < 23) begin
                                                                             //delays pixel data so
                       positions match up with correct data
322
                      converter_delay <= converter_delay + 1;</pre>
323
                      pixel_addr_to_hsv <= pixel_addr_to_hsv + 1;</pre>
324
                  end else if (converter_delay == 23) begin
325
                      extraction_start <= 1;</pre>
326
                      pixel_addr_to_hsv <= pixel_addr_to_hsv + 1;</pre>
```

```
327
                     converter delay <= converter delay + 1;
328
                 end else if (extraction_x == CAM_WIDTH-1 && extraction_y == CAM_HEIGHT-1)
                     begin //finished case
329
                     counting <= 0;
330
                 end else begin
331
                     if (extraction_x == CAM_WIDTH - 1) begin
                                                                        //next line
332
                          extraction_x <= 0;</pre>
333
                          extraction_y <= extraction_y + 1;</pre>
334
                     end else extraction_x <= extraction_x + 1;</pre>
                                                                        //next pixel
335
                     extraction_start <= 0;</pre>
336
                     pixel_addr_to_hsv <= pixel_addr_to_hsv + 1;</pre>
337
                 end
338
             end
339
340
         end
341
342
         assign pixel_addr_out = hcount+vcount*32'd320;
343
         assign cam = ((hcount<320) && (vcount<240))?frame_buff_out:12'h000;
344
345
       //instantiates camera reading module
346
       camera_read my_camera(.p_clock_in(pclk_in),
347
                                .vsync_in(vsync_in),
                                .href_in(href_in),
348
349
                                .p_data_in(pixel_in),
350
                                .pixel_data_out(output_pixels),
351
                                .pixel_valid_out(valid_pixel),
352
                                .frame_done_out(frame_done_out));
353
354
         //uncommenting below shows the white grid
355
         wire border = (hcount==0 | hcount==10 | hcount==20 | hcount==30 | hcount==40 |
            hcount==50 | hcount==60 | hcount==70 | hcount==80 | hcount==90 | hcount==100 |
            hcount==110 | hcount==120 | hcount==130 | hcount==140 | hcount==150 | hcount
            ==160 | hcount==170 | hcount==180 | hcount==190 | hcount==200 | hcount==210 |
            hcount==220 | hcount==230 | hcount==240 | hcount==250 | hcount==260 | hcount
            ==270 | hcount==280 | hcount==200 | hcount==300 | hcount==310 | vcount==0 |
            vcount==15 | vcount==30 | vcount==45 | vcount==60 | vcount==75 | vcount==90 |
            vcount==105 | vcount==120 | vcount==135 | vcount==165 | vcount
            ==180 | vcount==195 | vcount==210 | vcount==225);
356
357
         //display a colored crosshair for each position depending on the color detected
358
         wire border_0 = {hcount == (last_pos0[8:4]*10) | vcount == (last_pos0[3:0]*15)};
359
         wire border_1 = {hcount == (last_pos1[8:4]*10) | vcount == (last_pos1[3:0]*15)};
360
         wire border_2 = {hcount == (last_pos2[8:4]*10) | vcount == (last_pos2[3:0]*15)};
361
         wire border_3 = {hcount == (last_pos3[8:4]*10) | vcount == (last_pos3[3:0]*15)};
362
         wire border_4 = {hcount == (last_pos4[8:4]*10) | vcount == (last_pos4[3:0]*15)};
363
         wire border_5 = {hcount == (last_pos5[8:4]*10) | vcount == (last_pos5[3:0]*15)};
364
365
         reg b, hs, vs;
366
         always_ff @(posedge clk_65mhz) begin
367
              hs <= hsync;
368
              vs <= vsync;
369
              b <= blank;</pre>
370
              if (extraction_done) begin
371
                 last_pos0 <= led_positions[0];</pre>
372
                 last_pos1 <= led_positions[1];</pre>
373
                 last_pos2 <= led_positions[2];</pre>
374
                 last_pos3 <= led_positions[3];</pre>
375
                 last_pos4 <= led_positions[4];</pre>
376
                 last_pos5 <= led_positions[5];</pre>
```

```
377
             end
378
379
             //each of these conditionals handles assigning a color to a crosshair
380
             if (border_0) begin
381
                case (last_pos0[11:9])
382
                            : rgb <= {4'b1111, 8'b0};
                    RED
383
                    BLUE
                            :
                                rgb <= {8'b0, 4'b1111};
384
                    GREEN : rgb <= {4'b0, 4'b1111, 4'b0};
385
                    ORANGE :
                               rgb <= {4'b1111, 4'd9, 4'b0};
386
                    default :
                                 ;
387
                endcase
388
             end else if (border_1) begin
389
                case (last_pos1[11:9])
390
                           : rgb <= {4'b1111, 8'b0};
                    RED
                           : rgb <= {8'b0, 4'b1111};
391
                    BLUE
392
                    GREEN : rgb <= {4'b0, 4'b1111, 4'b0};
393
                    ORANGE : rgb <= {4'b1111, 4'd9, 4'b0};
394
                    default :
                                ;
395
                endcase
396
             end else if (border_2) begin
397
                case (last_pos2[11:9])
398
                            : rgb <= {4'b1111, 8'b0};
                    RED
399
                            : rgb <= {8'b0, 4'b1111};
                    BLUE
400
                               rgb <= {4'b0, 4'b1111, 4'b0};
                    GREEN
                            :
                    ORANGE :
                               rgb <= {4'b1111, 4'd9, 4'b0};
401
402
                    default :
                                ;
403
                endcase
404
             end else if (border_3) begin
405
                case (last_pos3[11:9])
406
                                rgb <= {4'b1111, 8'b0};
                    RED
                            :
407
                               rgb <= {8'b0, 4'b1111};
                    BLUE
                            :
408
                    GREEN : rgb <= {4'b0, 4'b1111, 4'b0};
409
                    ORANGE : rgb <= {4'b1111, 4'd9, 4'b0};
410
                    default :
                                 ;
411
                endcase
412
             end else if (border_4) begin
413
                case (last_pos4[11:9])
414
                            : rgb <= {4'b1111, 8'b0};
                    RED
415
                    BLUE
                                rgb <= {8'b0, 4'b1111};
                            :
                    GREEN
416
                                rgb <= {4'b0, 4'b1111, 4'b0};
                            :
                    ORANGE : rgb <= {4'b1111, 4'd9, 4'b0};
417
418
                    default :
                                ;
419
                endcase
420
             end else if (border_5) begin
421
                case (last_pos5[11:9])
422
                    RED
                           : rgb <= {4'b1111, 8'b0};
423
                            : rgb <= {8'b0, 4'b1111};
                    BLUE
424
                           : rgb <= {4'b0, 4'b1111, 4'b0};
                    GREEN
425
                    ORANGE : rgb <= {4'b1111, 4'd9, 4'b0};
426
                    default :
                                ;
427
                endcase
428
             end else if (border && (vcount < 240 && hcount < 320)) begin
429
                rgb <= {12{1'b1}};
430
             end else begin
431
                rgb <= cam | text_pixel_out;</pre>
432
             end
433
        end
434
435
        // the following lines are required for the Nexys4 VGA circuit - do not change
```

```
436
       assign vga_r = ~b ? rgb[11:8]: 0;
437
       assign vga_g = ~b ? rgb[7:4] : 0;
438
       assign vga_b = ~b ? rgb[3:0] : 0;
439
440
       assign vga_hs = ~hs;
441
       assign vga_vs = ~vs;
442
443
   endmodule
444
445 module synchronize #(parameter NSYNC = 3) // number of sync flops. must be >= 2
446
                    (input clk, in,
447
                    output reg out);
448
449
     reg [NSYNC-2:0] sync;
450
451
     always_ff 0 (posedge clk)
452
     begin
453
      {out,sync} <= {sync[NSYNC-2:0],in};</pre>
454
     end
455 endmodule
456
458 //
459
   // Pushbutton Debounce Module (video version - 24 bits)
460
   11
   461
462
463
   module debounce (input reset_in, clock_in, noisy_in,
464
                 output reg clean_out);
465
466
      reg [19:0] count;
467
      reg new_input;
468
469
      always_ff @(posedge clock_in)
470
       if (reset_in) begin
471
          new_input <= noisy_in;</pre>
472
          clean_out <= noisy_in;</pre>
473
          count <= 0; end
       else if (noisy_in != new_input) begin new_input<=noisy_in; count <= 0; end
474
475
       else if (count == 650000) clean_out <= new_input;</pre>
476
       else count <= count+1;
477
478
479
   endmodule
480
482 // Engineer: g.p.hom
483 //
484 // Create Date:
                   18:18:59 04/21/2013
485
   // Module Name:
                   display_8hex
   // Description: Display 8 hex numbers on 7 segment display
486
487
   11
   488
489
490 module display_8hex(
491
       input clk_in,
                                // system clock
492
       input [31:0] data_in,
                                // 8 hex numbers, msb first
493
       output reg [6:0] seg_out,
                                // seven segment display output
494
      output reg [7:0] strobe_out
                                // digit strobe
```

```
495
         );
496
497
         localparam bits = 13;
498
499
         reg [bits:0] counter = 0; // clear on power up
500
501
         wire [6:0] segments [15:0]; // 16 7 bit memorys
         assign segments[0] = 7'b100_0000; // inverted logic
502
503
         assign segments[1] = 7'b111_1001;
                                                // qfedcba
         assign segments[2] = 7'b010_0100;
504
505
         assign segments[3] = 7'b011_0000;
506
                              = 7'b001_{1001};
         assign segments[4]
507
         assign segments[5]
                              = 7' b001_{0010};
508
         assign segments[6]
                              = 7'b000_{0010};
509
         assign segments[7]
                              = 7' b111_{1000};
510
         assign segments[8] = 7'b000_0000;
511
         assign segments[9] = 7'b001_1000;
512
         assign segments[10] = 7'b000_1000;
513
         assign segments[11] = 7'b000_0011;
514
         assign segments[12] = 7'b010_0111;
515
         assign segments[13] = 7'b010_0001;
         assign segments[14] = 7'b000_0110;
516
517
         assign segments [15] = 7'b000_{1110};
518
519
         always_ff @(posedge clk_in) begin
520
           // Here I am using a counter and select 3 bits which provides
521
           // a reasonable refresh rate starting the left most digit
522
           // and moving left.
523
           counter <= counter + 1;
524
           case (counter[bits:bits-2])
525
                3'b000: begin // use the MSB 4 bits
526
                        seq_out <= segments[data_in[31:28]];</pre>
527
                        strobe_out <= 8'b0111_1111 ;</pre>
528
                       end
529
                3'b001: begin
530
531
                        seg_out <= segments[data_in[27:24]];</pre>
532
                        strobe_out <= 8'b1011_1111 ;</pre>
533
                       end
534
535
               3'b010: begin
536
                         seg_out <= segments[data_in[23:20]];</pre>
                         strobe_out <= 8'b1101_1111 ;</pre>
537
538
                        end
                3'b011: begin
539
540
                        seg_out <= segments[data_in[19:16]];</pre>
541
                        strobe_out <= 8'b1110_1111;</pre>
542
                       end
543
                3'b100: begin
544
                        seg_out <= segments[data_in[15:12]];</pre>
545
                        strobe_out <= 8'b1111_0111;</pre>
546
                       end
547
548
                3'b101: begin
549
                        seg_out <= segments[data_in[11:8]];</pre>
550
                        strobe_out <= 8'b1111_1011;</pre>
551
                       end
552
553
               3'b110: begin
```

554 seg out <= segments[data in[7:4]];</pre> 555 strobe_out <= 8'b1111_1101;</pre> 556 end 557 3'b111: begin 558 seq_out <= segments[data_in[3:0]];</pre> 559 strobe_out <= 8'b1111_110;</pre> 560 end 561 562 endcase 563 end 564 565 endmodule 566 _____ 567 568 // Update: 8/8/2019 GH 569 // Create Date: 10/02/2015 02:05:19 AM 570 // Module Name: xvga 571 // 572 // xvga: Generate VGA display signals (1024 x 768 @ 60Hz) 573 // 574 // ---- HORIZONTAL ----------VERTICAL -----575 // Active Active 576 // FP Sync FP Sync BP Video BPVideo Freq 577 // 25.175 640 96 2 640x480, 60Hz 16 48 480 11 .31 578 // 800x600, 60Hz 40.000 40 128 600 1 4 23 800 88 1024x768, 60Hz 579 11 3 65.000 1024 24 136 160 768 6 29 1280x1024, 60Hz 108.00 580 11 1280 48 112 248 768 1 3 38 581 1280 72 80 216 720 3 30 11 1280x720p 60Hz 75.25 .5 582 // 1920x1080 60Hz 148.5 1920 88 44 148 1080 4 5 36 583 // 584 // change the clock frequency, front porches, sync's, and back porches to create 585 // other screen resolutions 587 588 module xvga(input vclock_in, 589 output reg [10:0] hcount_out, // pixel number on current line 590 // line number output reg [9:0] vcount_out, 591 output reg vsync_out, hsync_out, 592 output reg blank_out); 593 594 parameter DISPLAY_WIDTH = 1024; // display width 595 parameter DISPLAY_HEIGHT = 768; // number of lines 596 597 parameter $H_FP = 24;$ // horizontal front porch 598 parameter H_SYNC_PULSE = 136; // horizontal sync 599 parameter $H_BP = 160;$ // horizontal back porch 600 601 parameter $V_{FP} = 3;$ // vertical front porch 602 parameter V_SYNC_PULSE = 6; // vertical sync 603 parameter $V_BP = 29;$ // vertical back porch 604 605 // horizontal: 1344 pixels total 606 // display 1024 pixels per line 607 reg hblank, vblank; 608 wire hsyncon, hsyncoff, hreset, hblankon; 609 assign hblankon = (hcount_out == (DISPLAY_WIDTH -1)); 610 assign hsyncon = (hcount_out == (DISPLAY_WIDTH + H_FP - 1)); //1047 assign hsyncoff = (hcount_out == (DISPLAY_WIDTH + H_FP + H_SYNC_PULSE - 1)); // 611 1183

```
612
        assign hreset = (hcount_out == (DISPLAY_WIDTH + H_FP + H_SYNC_PULSE + H_BP - 1));
           //1343
613
614
        // vertical: 806 lines total
615
        // display 768 lines
616
        wire vsyncon, vsyncoff, vreset, vblankon;
617
        assign vblankon = hreset & (vcount_out == (DISPLAY_HEIGHT - 1)); // 767
618
       assign vsyncon = hreset & (vcount_out == (DISPLAY_HEIGHT + V_FP - 1)); // 771
        assign vsyncoff = hreset & (vcount_out == (DISPLAY_HEIGHT + V_FP + V_SYNC_PULSE -
619
           1)); // 777
620
        assign vreset = hreset & (vcount_out == (DISPLAY_HEIGHT + V_FP + V_SYNC_PULSE +
           V_BP - 1)); // 805
621
622
       // sync and blanking
623
       wire next_hblank,next_vblank;
624
       assign next_hblank = hreset ? 0 : hblankon ? 1 : hblank;
625
       assign next_vblank = vreset ? 0 : vblankon ? 1 : vblank;
626
       always_ff @(posedge vclock_in) begin
627
          hcount_out <= hreset ? 0 : hcount_out + 1;</pre>
628
           hblank <= next_hblank;</pre>
629
          hsync_out <= hsyncon ? 0 : hsyncoff ? 1 : hsync_out; // active low</pre>
630
631
           vcount_out <= hreset ? (vreset ? 0 : vcount_out + 1) : vcount_out;</pre>
632
           vblank <= next_vblank;</pre>
633
           vsync_out <= vsyncon ? 0 : vsyncoff ? 1 : vsync_out; // active low</pre>
634
635
           blank_out <= next_vblank | (next_hblank & ~hreset);</pre>
636
       end
637
638 endmodule
```

Camera Read

```
module camera_read(
1
2
            input p_clock_in,
3
            input vsync_in,
4
            input href_in,
5
            input [7:0] p_data_in,
6
            output logic [15:0] pixel_data_out,
7
            output logic pixel_valid_out,
8
            output logic frame_done_out
9
        );
10
11
12
            logic [1:0] FSM_state = 0;
13
        logic pixel_half = 0;
14
15
            localparam WAIT_FRAME_START = 0;
16
            localparam ROW_CAPTURE = 1;
17
18
19
            always_ff@(posedge p_clock_in)
20
            begin
21
            case(FSM_state)
22
23
            WAIT_FRAME_START: begin //wait for VSYNC
24
                FSM_state <= (!vsync_in) ? ROW_CAPTURE : WAIT_FRAME_START;</pre>
25
                frame_done_out <= 0;</pre>
26
                pixel_half <= 0;</pre>
27
            end
28
29
            ROW_CAPTURE: begin
30
                FSM_state <= vsync_in ? WAIT_FRAME_START : ROW_CAPTURE;</pre>
31
                frame_done_out <= vsync_in ? 1 : 0;</pre>
32
                pixel_valid_out <= (href_in && pixel_half) ? 1 : 0;</pre>
33
                if (href_in) begin
                    pixel_half <= ~ pixel_half;</pre>
34
35
                    if (pixel_half) pixel_data_out[7:0] <= p_data_in;</pre>
36
                    else pixel_data_out[15:8] <= p_data_in;</pre>
37
                end
38
            end
39
            endcase
40
            end
41
42 endmodule
```

RGB to HSV Converter

```
'timescale 1ns / 1ps
 1
3 // Company:
4 // Engineer: Kevin Zheng Class of 2012
5 //
               Dept of Electrical Engineering & Computer Science
6 //
7 // Create Date:
                    18:45:01 11/10/2010
8 // Design Name:
9 // Module Name:
                    rgb2hsv
10 // Project Name:
11
   // Target Devices:
12 // Tool versions:
13 // Description:
14 //
15 // Dependencies:
16 //
17 // Revision:
18 // Revision 0.01 - File Created
19 // Additional Comments:
20 //
22 module rgb2hsv(clock, reset, r, g, b, h, s, v);
23
                 input wire clock;
24
                 input wire reset;
25
                 input wire [7:0] r;
26
                 input wire [7:0] g;
27
                 input wire [7:0] b;
28
                 output reg [7:0] h;
29
                 output reg [7:0] s;
30
                 output reg [7:0] v;
31
                 reg [7:0] my_r_delay1, my_g_delay1, my_b_delay1;
32
                 reg [7:0] my_r_delay2, my_g_delay2, my_b_delay2;
33
                 reg [7:0] my_r, my_g, my_b;
34
                 reg [7:0] min, max, delta;
35
                 reg [15:0] s_top;
                 reg [15:0] s_bottom;
36
37
                 reg [15:0] h_top;
38
                 reg [15:0] h_bottom;
39
                 wire [15:0] s_quotient;
40
                 wire [15:0] s_remainder;
41
                 wire s_rfd;
42
                 wire [15:0] h_quotient;
43
                 wire [15:0] h_remainder;
44
                 wire h_rfd;
45
                 reg [7:0] v_delay [19:0];
                 reg [18:0] h_negative;
46
47
                 reg [15:0] h_add [18:0];
48
                 reg [4:0] i;
49
                 reg [31:0] hue_out;
50
                 wire [17:0] s_res;
51
                 wire [17:0] h_res;
52
53
                 wire dout_valid_h;
54
                 wire dout_valid_s;
55
56
                 // Clocks 4-18: perform all the divisions
```

57			//the s_divider (16/16) has delay 18
58			//the hue div (16/16) has delay 18
59			
60	11		divider hue div1(
61	11		
62	11		dividend(s top)
63	11		divisor(a bottom)
64	11		mution(s_butchin)
65	11		.quotient(5_quotient),
05	//	1 1 1 -	// note. the fractional output was originarry named remainder in
~		LNIS	// c'l.
00			// file it seems coregen will name this output "fractional" even if
0/			// you alan't select the remainder type as fractional.
68	//		. Iractional (s_remainder),
69	11		.rtd(s_rtd)
70	//);
71		div_gen	_0 hue_div1(
72			.aclk(clock),
73			.s_axis_dividend_tdata(s_top),
74			.s_axis_dividend_tvalid(1),
75			.s_axis_divisor_tdata(s_bottom),
76			.s_axis_divisor_tvalid(1),
77			.m_axis_dout_tdata(s_res),
78			<pre>// note: the "fractional" output was originally named "remainder" in</pre>
			this
79			<pre>// file it seems coregen will name this output "fractional" even if</pre>
80			<pre>// you didn't select the remainder type as fractional.</pre>
81			.m_axis_dout_tvalid(dout_valid_s)
82);
83			assign s_quotient = s_res[17:2];
84			assign s_remainder = s_res[1:0];
85			
86			div gen 0 hue div2(
87			.aclk(clock),
88			.s axis dividend tdata(h top),
89			.s axis dividend tvalid(1),
90			.s axis divisor tdata(h bottom),
91			s axis divisor tvalid(1).
92			maxis dout tdata(h res).
93			.m_axis_dout_tvalid(dout_valid_h)
94):
95			
96			assign h quotient = h res[17:2]:
97			assign h_quotiente h_res[1:0]:
98			
90	11		divider hue div?(
100	11		alk(alock)
100	11		dividend (h top)
101	11		divisor(h bottom)
102	11		<pre>.divisor(<u>II_bottom</u>), guetient(h_guetient)</pre>
103			
104			. Flactional (<u>I_</u> remainder),
105			. <i>r</i> 1 <i>a</i> (<i>n_r</i> 1 <i>a</i>)
107	//		//
107			arways @ (poseage crock) begin
100			
109			// CLOCK I: LATCH THE INPUTS (Always positive)
110			{my_r, my_g, my_b} <= {r, g, b};
111			
112			// Clock 2: compute min, max
113			<pre>{my_r_delay1, my_g_delay1, my_b_delay1} <= {my_r, my_g, my_b};</pre>

114	
115	if((mv r) = mv q) & (mv r) = mv b)) //(B.S.S)
116	max <= mv r:
117	else if $(my \alpha \ge my r)$ & $(my \alpha \ge my h)) //(S B S)$
118	$max \leq my \ \alpha$
110	max < my g
120	$erse \max \ <- \ \max \ y_0,$
120	$\frac{1}{2} \left(\frac{1}{2} - \frac{1}{2} + \frac{1}{2} \right) = \frac{1}{2} \left(\frac{1}{2} - \frac{1}{2} + \frac{1}{2} +$
121	$II((IIIY_I <- IIIY_G) \&\& (IIIY_I <- IIIY_G)) //(S, B, B)$
122	$\min <= m \underline{y} \underline{r};$
123	else if((my_g <= my_r) && (my_g <= my_b)) //(B,S,B)
124	<pre>min <= my_g;</pre>
125	else
126	min <= my_b;
127	
128	// Clock 3: compute the delta
129	<pre>{my_r_delay2, my_g_delay2, my_b_delay2} <= {my_r_delay1, my_g_delay1, my_b_delay1};</pre>
130	<pre>v_delay[0] <= max;</pre>
131	delta <= max - min;
132	,
133	// Clock 4: compute the top and bottom of whatever divisions
134	we need to do
125	$s_{cop} < 0$ using \star using {\star} using
133	S_DOLLOW <- (V_detay[0]>0):{8.d0, V_detay[0]}: 18.d1;
130	
13/	if (mu u deleus)
138	<pre>if(my_r_delay2 == v_delay[0]) begin</pre>
139	<pre>n_top <= (my_g_delay2 >= my_b_delay2)?(my_g_delay2 - my_b_delay2) * 8'd255:(my_b_delay2 - my_g_delay2) * 8'd255;</pre>
140	h_negative[0] <= (my_g_delay2 >= my_b_delay2)?0:1;
141	h_add[0] <= 16'd0;
142	end
143	else if(my_q_delay2 == v_delay[0]) begin
144	h_top <= (my_b_delay2 >= my_r_delay2)?(my_b_delay2 -
	<pre>my_r_delay2) * 8'd255:(my_r_delay2 - my_b_delay2) *</pre>
145	h negative[0] <= (my h delay2 >= my r delay2)?0:1:
146	h_negacive[0] $\langle (my_b_aciay2) \rangle my_f_aciay2).0.1$
147	n_add[0] <= 10 d00,
147	elae if(my, h, delay) = y delay(0) hegin
140	erse rr($my_D_deray2 = \sqrt{deray[0]}$) begin h top $(-\sqrt{my_D_deray2}) = my_d delay2) 2(my_D_deray2) = -$
147	<pre>my_g_delay2 > my_g_delay2 - my_r_delay2 = my_g_delay2) * 8'd255:(my_g_delay2 - my_r_delay2) * 8'd255.</pre>
150	h negative[0] <= (mu r delau? >= mu g delau?) 20.1 .
151	$\frac{1}{1} - \frac{1}{1} - \frac{1}$
151	$\Pi_{add}[0] <= 10^{\circ} d1/0$
152	ena
155	
154	$n_{\text{Dollom}} <= (delta > 0) / delta * 8. d6:16. d6;$
155	
150	
15/	//delay the V and n_negative signals 18 times
150	lor(1=1; 1<19; 1=1+1) begin
109	<pre>v_delay[1] <= v_delay[1-1];</pre>
100	<pre>h_negative[i] <= h_negative[i-1];</pre>
101	h_add[i] <= h_add[i-1];
162	end
163	
164	v_delay[19] <= v_delay[18];

165		//Clock 22: compute the final value of h
166		<pre>//depending on the value of h_delay[18], we need to subtract 255 from it to make it come back around the circle</pre>
167		if(h_negative[18] && (h_quotient > h_add[18]))
168		h <= 8'd255 - h_quotient[7:0] + h_add[18];
169		end
170		else if(h_negative[18]) begin
171		<pre>h <= h_add[18] - h_quotient[7:0];</pre>
172		end
173		else begin
174		<pre>h <= h_quotient[7:0] + h_add[18];</pre>
175		end
176		
177		<pre>//pass out s and v straight</pre>
178		s <= s_quotient;
179		v <= v_delay[19];
180	end	
181	endmodule	

LED Position Extraction

```
'timescale 1ns / 1ps
1
2
3 module led_position_extraction(
4
                                     input clock,
5
                                     input reset,
6
                                     input start,
7
                                     input [7:0] h,
8
                                     input [7:0] s,
9
                                     input [7:0] v,
10
                                     input [8:0] x,
11
                                     input [7:0] y,
                                     input [7:0] red_threshold,
12
13
                                     input [7:0] blue_threshold,
                                     input [7:0] green_threshold,
14
15
                                     input [7:0] orange_threshold,
16
                                     input [7:0] red_lower,
17
                                     input [7:0] red_upper,
18
                                     input [7:0] blue_lower,
19
                                     input [7:0] blue_upper,
20
                                     input [7:0] green_lower,
21
                                     input [7:0] green_upper,
22
                                     input [7:0] orange_lower,
23
                                     input [7:0] orange_upper,
24
                                     input [7:0] value_threshold,
25
                                     output logic [11:0] led_positions [5:0],
26
                                     output logic done,
27
                                     output logic ready
28
       );
29
       parameter BIN_WIDTH = 10;
                                                  //width of each bin in pixels
30
       parameter BIN_HEIGHT = 15;
                                                 //height of each bin in pixels
31
       parameter NUM_BINS_X = 32;
                                                 //number of bins in the x direction
32
       parameter NUM_BINS_Y = 16;
                                                  //number of bins in the y direction
33
       parameter CAM_WIDTH = 320;
34
       parameter CAM_HEIGHT = 240;
35
   11
         parameter RED_THRESHOLD = 75;
   11
36
         parameter BLUE_THRESHOLD = 75;
   11
37
         parameter GREEN_THRESHOLD = 75;
38
   11
         parameter ORANGE_THRESHOLD = 75;
39
        localparam NO_COLOR = 0;
40
        localparam RED = 1;
41
        localparam BLUE = 2;
42
        localparam GREEN = 3;
43
        localparam ORANGE = 4;
44
45
        //tallies the number of red pixels found in the bin currently being accessed
46
        logic [3:0] temp_red_count;
47
        logic [3:0] temp_blue_count;
48
        logic [3:0] temp_green_count;
49
        logic [3:0] temp_orange_count;
50
51
        logic [2:0] current_pixel_color;
                                                 //color of the currently observed pixel
52
53
        //total value to be stored in the pixel count BRAM
54
        logic [7:0] value_to_red_bram;
55
        logic [7:0] value_to_blue_bram;
56
        logic [7:0] value_to_green_bram;
```

```
57
        logic [7:0] value_to_orange_bram;
58
59
        //previous total value from the pixel count BRAM
60
        logic [7:0] value_from_red_bram;
61
        logic [7:0] value_from_blue_bram;
62
        logic [7:0] value_from_green_bram;
63
        logic [7:0] value_from_orange_bram;
64
65
        logic valid_values;
                                                 //true when the last pixel of the current
            bin has been counted
66
        logic [8:0] bin_bram_address;
                                                 //muxed bin address
67
68
 69
        logic [5:0] x_counter;
                                                  //counter used to track intra-bin position
 70
        logic [8:0] x_delay;
                                                  //used to delay x 1 cycle
71
72
        logic cell_determiner_done;
                                                 //true once all bins have been summed
73
74
        logic [8:0] cell_determiner_address;
                                                 //address of the bin current being summed
75
        logic [8:0] finished_bin_address;
                                                 //addresses bins after they have bin
            tallied to determine their overall color
 76
77
        logic [2:0] color_grid [15:0][31:0];
                                                 //16x32 array used to avoid double
            counting of leds
78
        logic [4:0] x_pos;
                                                  //x position used to access color_grid
79
        logic [3:0] y_pos;
                                                  //y position used to access color_grid
80
81
        //used to reset color_grid
82
        integer i;
83
        integer j;
84
85
        //used to reset led positions
86
        integer k;
87
88
        logic [1:0] delay_counter;
                                                 //added delay to account for BRAM access
            latency
89
        logic [2:0] output_counter;
                                                 //counts the number of led positions that
            have been assigned a position
 90
 91
        logic started;
                                                  //true after the module has been started
92
        logic internal_reset;
                                                  //used to reset the module after a frame
            has been processed in preparation for the next fram
93
        logic cell_determiner_reset;
                                                 //resets the cell_determiner module
 94
        logic resetting;
                                                 //true when the module is interally
            resetting
 95
 96
        assign cell_determiner_reset = (internal_reset || reset);
 97
        assign bin_bram_address = (cell_determiner_done) ? finished_bin_address :
            cell_determiner_address;
98
99
        //Instantiates cell_detmerminer module
100
        //Determines the BRAM address for the current bin
101
        cell_determiner #(.BIN_WIDTH(BIN_WIDTH), .BIN_HEIGHT(BIN_HEIGHT), .NUM_BINS_X(
            NUM_BINS_X), .NUM_BINS_Y(NUM_BINS_Y),. CAM_WIDTH(CAM_WIDTH),
102
            .CAM_HEIGHT(CAM_HEIGHT)) my_cell_determiner (.clock(clock), .reset(
                cell_determiner_reset), .start(start), .x(x), .y(y),
103
             .bin_num(cell_determiner_address), .done(cell_determiner_done));
104
105
        //Instantiates pixel_color_threshold module
```

```
106
         //Determines the color of the current pixel
107
        pixel_color_threshold my_color_threshold (.h(h), .v(v), .color(current_pixel_color
            ), .red_lower(red_lower),
108
             .red_upper(red_upper), .blue_lower(blue_lower), .blue_upper(blue_upper), .
                 green_lower(green_lower),
109
             .green_upper(green_upper), .orange_lower(orange_lower), .orange_upper(
                 orange_upper),
110
             .value_threshold(value_threshold));
111
112
113
         //each of the following BRAMs tracks the count of pixels of each
114
         //respective color for each bin in the 16x32 grid
115
        blk_mem_gen_1 red_BRAM (
116
           .clka(clock),
117
           .wea(valid_values),
118
           .addra(bin_bram_address),
119
           .dina(value_to_red_bram),
120
           .clkb(clock),
121
           .rstb(0),
122
           .addrb(bin_bram_address),
123
           .doutb(value_from_red_bram),
124
           .rsta_busy(red_rsta_busy),
125
           .rstb_busy(red_rstb_busy)
126
        );
127
128
        blk_mem_gen_1 blue_BRAM (
129
           .clka(clock),
130
           .wea(valid_values),
131
           .addra(bin_bram_address),
132
           .dina(value_to_blue_bram),
133
           .clkb(clock),
134
           .rstb(0),
135
           .addrb(bin_bram_address),
136
           .doutb(value_from_blue_bram),
137
           .rsta_busy(blue_rsta_busy),
           .rstb_busy(blue_rstb_busy)
138
139
        );
140
141
        blk_mem_gen_1 green_BRAM (
142
           .clka(clock),
143
           .wea(valid_values),
144
           .addra(bin_bram_address),
145
           .dina(value_to_green_bram),
146
           .clkb(clock),
147
           .rstb(0),
148
           .addrb(bin_bram_address),
149
           .doutb(value_from_green_bram),
150
           .rsta_busy(green_rsta_busy),
151
           .rstb_busy(green_rstb_busy)
152
        );
153
154
        blk_mem_gen_1 orange_BRAM (
155
           .clka(clock),
156
           .wea(valid_values),
157
           .addra(bin_bram_address),
158
           .dina(value_to_orange_bram),
159
           .clkb(clock),
160
           .rstb(0),
161
           .addrb(bin_bram_address),
```

```
162
            .doutb(value_from_orange_bram),
163
            .rsta_busy(orange_rsta_busy),
164
            .rstb_busy(orange_rstb_busy)
165
          );
166
167
         always_ff @(posedge clock) begin
168
              if (reset || internal_reset) begin
169
                   temp_red_count <= 0;</pre>
170
                   temp_blue_count <= 0;</pre>
171
                   temp_green_count <= 0;</pre>
172
                   temp_orange_count <= 0;</pre>
173
                   x_counter <= 0;</pre>
174
                   valid_values <= 0;</pre>
175
                   finished_bin_address <= 0;</pre>
176
177
                   //resets the color grid
178
                   for (i=0; i<NUM_BINS_Y; i=i+1) begin</pre>
179
                        for (j=0; j<NUM_BINS_X; j=j+1) begin</pre>
180
                             color_grid[i][j] <= 3'b0;</pre>
181
                        end
182
                   end
183
                   x_pos <= 0;
184
                   y_pos <= 0;
185
                   delay_counter <= 0;</pre>
186
                   output_counter <= 0;</pre>
187
                   done <= 0;
188
                   started <= 0;</pre>
189
                   internal_reset <= 0;</pre>
190
191
                   //reset the output array to 0
192
                   for (k=0; k<6; k=k+1) begin
193
                        led_positions[k] <= 12'b0;</pre>
194
                   end
195
                   resetting <= 0;</pre>
196
                   ready <= 1;</pre>
197
              end else begin
198
                   if (start) begin
199
                        started <= 1;
200
                        ready <= 0;</pre>
201
                   end
202
                   if (done || resetting) begin
203
                        if (done) begin
204
                            resetting <= 1;</pre>
205
                             finished_bin_address <= 0;</pre>
206
                             done <= 0;
207
                            valid_values <= 1;</pre>
208
                        end else if (finished_bin_address < 511) begin</pre>
209
                             finished_bin_address <= finished_bin_address + 1;</pre>
210
                        end else begin
211
                             internal_reset <= 1;</pre>
212
                        end
213
214
                   end
215
                   if (start || started) begin
216
                        if (output_counter > 5) begin //Once all 6 outputs have been
                            determined
217
                             done <= 1;
218
                             started <= 0;</pre>
219
                        end
```

220	
221	if (cell_determiner_done) begin //After all pixels have been interated
	over, start interating over bins
222	value to red bram ≤ 0 :
223	value to blue bram <= 0:
224	value to green bram <= 0.
225	value to orange bram <= 0.
225	finished hin address <= finished hin address + 1.
220	if (dolay counter < 2) begin (/Dolay to account for RPAM road time
221	If (detay_conter < 2) begin //beray to account to bran fead time
228	; matches the value from BRAW to the correct x, y position
220	delay_counter <- delay_counter + 1;
229	end else begin
230	II (x_pos == NOM_BINS_X-I) begin //updates the x,y position for
221	the current bin being accessed from BRAM
231	x_pos <= 0;
232	y_pos <= y_pos + 1;
233	end else x_pos <= x_pos + 1;
234	
235	<pre>//Classify bin based on num of colored pixels of one color</pre>
236	<pre>//if num is > threshold for that color, count the bin</pre>
237	//color priority is red -> blue -> green -> orange
238	if (value_from_red_bram > red_threshold)
239	//Checks that no surrounding bins are the same color;
	eliminates the possibility of double counting one LED
240	if (color_grid[y_pos-1][x_pos]!=RED && color_grid[y_pos
	+1][x_pos]!=RED && color_grid[y_pos][x_pos+1]!=RED &&
	color_grid[y_pos][x_pos-1]!=RED)
241	<pre>color_grid[y_pos][x_pos] <= RED;</pre>
242	<pre>led_positions[output_counter] <= {3'd1, x_pos, y_pos};</pre>
243	<pre>output_counter <= output_counter + 1;</pre>
244	end i _ i _ i
245	end else if (value from blue bram $>$ blue threshold) begin
246	if (color gridly pos-11/x pos) = BLUE && color gridly pos
	+11[x pos]'=BLUE && color grid[y pos][x pos+1]'=BLUE &&
	color grid(v pos) (x pos-1) '= BLUE) begin
247	color grid[y pos](x pos] <= BLUE:
248	$\int \frac{d^2}{dt} = \int $
249	output counter (counter + 1.
250	and
250	end also if (value from green bram > green threshold) begin
251	end erse if (value_from_green_bran > green_chreshold, begin
232	11 (COLOF_GFLG[y_pos=1][x_pos]:-GREEN && COLOF_GFLG[y_pos
	+1][x_pos]:-GREEN && COLOT_GILd[y_pos][x_pos+1]:-GREEN
252	&& color_grid[y_pos][x_pos-1]!=GREEN) begin
255	color_grid[y_pos][x_pos] <= GREEN;
254	<pre>led_positions[output_counter] <= {3'd3, x_pos, y_pos};</pre>
255	<pre>output_counter <= output_counter + 1;</pre>
256	end
257	end else if (value_from_orange_bram > orange_threshold) begin
258	if (color_grid[y_pos-1][x_pos]!=ORANGE && color_grid[y_pos
	+1][x_pos]!=ORANGE && color_grid[y_pos][x_pos+1]!=
	ORANGE && color_grid[y_pos][x_pos-1]!=ORANGE)
259	<pre>color_grid[y_pos][x_pos] <= ORANGE;</pre>
260	<pre>led_positions[output_counter] <= {3'd4, x_pos, y_pos};</pre>
261	<pre>output_counter <= output_counter + 1;</pre>
262	end
263	end else ; //no color in the bin was above the threshold
264	if (x_pos == NUM_BINS_X-1 && y_pos == NUM_BINS_Y-1)
265	done <= 1;
266	<pre>started <= 0;</pre>

267 268	end end
269	end else begin //iterating over all the pixels from the camera (in
270	if (x_counter == BIN_WIDTH-1) begin //Adds the current amount of
271	valid values <= 1:
272	value to red bram <= (current pixel color == RED) ? (
	temp_red_count + 1) + value_from_red_bram : temp_red_count
	+ value_from_red_bram;
273	<pre>value_to_blue_bram <= (current_pixel_color == BLUE) ? (</pre>
	<pre>temp_blue_count + 1) + value_from_blue_bram :</pre>
	<pre>temp_blue_count + value_from_blue_bram;</pre>
274	<pre>value_to_green_bram <= (current_pixel_color == GREEN) ? (</pre>
	<pre>temp_green_count + 1) + value_from_green_bram :</pre>
	<pre>temp_green_count + value_from_green_bram;</pre>
275	<pre>value_to_orange_bram <= (current_pixel_color == ORANGE) ? (</pre>
	<pre>temp_orange_count + 1) + value_from_orange_bram :</pre>
276	<pre>temp_orange_count + value_from_orange_bram;</pre>
270	temp_red_count <= 0;
277	temp_dide_count <= 0;
270	temp orange count ≤ 0 .
280	x = 0:
281	A_councer < of
282	end else begin <i>//adds the current pixel to a temp value if it</i>
	falls into one of the four colors
283	<pre>valid_values <= 0;</pre>
284	<pre>x_counter <= x_counter + 1;</pre>
285	<pre>case (current_pixel_color)</pre>
286	<pre>RED : temp_red_count <= temp_red_count + 1;</pre>
287	BLUE : temp_blue_count <= temp_blue_count + 1;
288	<pre>GREEN : temp_green_count <= temp_green_count + 1;</pre>
289	<pre>ORANGE : temp_orange_count <= temp_orange_count + 1;</pre>
290	derault : ;
291	end
292	end
294	end
295	end
296	end
297	
298	endmodule
299	
300	
301	
302	nodule cell_determiner(
303	input clock,
304 205	input reset,
305	input [2:0] y
307	input $[7:0]$ v
308	output logic [8:0] bin num.
309	output logic done
310);
311	parameter BIN_WIDTH = 10;
312	<pre>parameter BIN_HEIGHT = 15;</pre>
313	<pre>parameter NUM_BINS_X = 32;</pre>
314	<pre>parameter NUM_BINS_Y = 16;</pre>

```
315
        parameter CAM WIDTH = 320;
316
        parameter CAM_HEIGHT = 240;
317
318
        logic [5:0] x_counter;
319
        logic [5:0] y_counter;
320
        logic [8:0] x_delay;
321
        logic started;
322
323
        always_ff @(posedge clock) begin
324
            if(reset) begin
325
                x_counter <= 0;</pre>
                y_counter <= 0;</pre>
326
327
                bin_num <= 0;</pre>
328
                done <= 0;
329
                started <= 0;
330
                x_delay <= 0;</pre>
331
            end else if (done) begin
332
                //do nothing
333
            end else if (start || started) begin
334
                if (start) started <= 1;
                                           //enter the started state after receiving a
                    start signal
335
                x_delay <= x;</pre>
                if (x_delay==CAM_WIDTH-1) begin
336
                                                                      //at the edge of the
                    fram
337
                    x_counter <= 1;</pre>
338
                    if (y == CAM_HEIGHT-1) begin
                                                                      //frame has been
                        finished
339
                        done <= 1;
340
                        started <= 0;</pre>
341
                    end else if (y_counter == BIN_HEIGHT-1) begin //next line is in a
                        different bin
342
                        y_counter <= 0;</pre>
343
                        bin_num <= bin_num + 1;</pre>
344
                    end else begin
                                                                      //next line is in the
                        same bin
345
                         y_counter <= y_counter + 1;</pre>
346
                        bin_num <= bin_num - (NUM_BINS_X-1);</pre>
347
                    end
348
                end else if (x_counter < BIN_WIDTH) begin</pre>
                                                                      //continues within the
                     same bin
349
                    x_counter <= x_counter + 1;</pre>
                                                                      //increments the bin
350
                end else if (x_counter == BIN_WIDTH) begin
                    counter once the edge of the current bin has been reached
351
                    x_counter <= 1;</pre>
352
                    bin_num <= bin_num + 1;</pre>
353
                end
354
            end
355
        end
356 endmodule
357
358
    359
360 module pixel_color_threshold (
361
                                     input [7:0] h,
362
                                     input [7:0] v,
363
                                     input [7:0] red_lower,
364
                                     input [7:0] red_upper,
365
                                     input [7:0] blue_lower,
366
                                     input [7:0] blue_upper,
```

```
367
                                       input [7:0] green_lower,
368
                                       input [7:0] green_upper,
369
                                       input [7:0] orange_lower,
370
                                       input [7:0] orange_upper,
371
                                       input [7:0] value_threshold,
372
                                       output logic [2:0] color
373
         );
374
         parameter RED LOWER THRESH = 248;
375
        parameter RED_UPPER_THRESH = 4;
376
        parameter BLUE_LOWER_THRESH = 145;
377
         parameter BLUE_UPPER_THRESH = 177;
378
         parameter GREEN_LOWER_THRESH = 57;
379
         parameter GREEN_UPPER_THRESH = 99;
380
         parameter ORANGE_LOWER_THRESH = 28;
381
         parameter ORANGE_UPPER_THRESH = 50;
382
         parameter VALUE_THRESHOLD = 153; //60% value
383
384
         localparam NO_COLOR = 0;
385
         localparam RED = 1;
386
         localparam BLUE = 2;
387
         localparam GREEN = 3;
388
         localparam ORANGE = 4;
389
390
         always_comb begin
391
             color = 0; //default
392
             if (v > value_threshold) begin
393
                 if (h > green_lower && h < green_upper) color = GREEN;
394
                 else if(h > red_lower || h < red_upper) color = RED;</pre>
395
                 else if (h > orange_lower && h < orange_upper) color = ORANGE;</pre>
396
                 else if (h > blue_lower && h < blue_upper) color = BLUE;
397
             end
398
         end
399
400
    endmodule
```

Finger Finder

```
'timescale 1ns / 1ps
1
2
3 module finger_finder(
4
                                       input clock,
5
                                       input reset,
6
                                       input [11:0] led_positions [5:0],
7
                                       input start,
8
                                       output logic done,
9
                                       output logic [8:0] thumb, //blue
10
                                       output logic [8:0] index, //orange
11
                                       output logic [8:0] middle, //green
                                       output logic [8:0] ring, //red
12
13
                                       output logic [8:0] pinkie, //blue
14
                                       output logic [8:0] palm //red
15
   );
16
        logic [11:0] pos_color [5:0];
17
        logic [3:0] count;
18
        logic [8:0] pos0;
19
        logic [2:0] col0;
20
        logic [8:0] pos1;
21
        logic [2:0] col1;
22
        logic [8:0] pos2;
23
        logic [2:0] col2;
24
        logic [8:0] pos3;
25
        logic [2:0] col3;
26
        logic [8:0] pos4;
27
        logic [2:0] col4;
28
        logic [8:0] pos5;
29
        logic [2:0] col5;
30
31
        localparam NO_COLOR = 0;
32
        localparam RED = 1;
33
        localparam BLUE = 2;
34
        localparam GREEN = 3;
35
        localparam ORANGE = 4;
36
37
        always_ff @(posedge clock) begin
38
            if (reset) begin //reset signal, new counter and values
39
                 count <= 0;</pre>
40
                 done <= 0;
41
                thumb <= 0;
42
                 index <= 0;
43
                middle <= 0;</pre>
44
                ring <= 0;
45
                pinkie <= 0;</pre>
46
                palm <= 0;
47
            end else if(start) begin //new values to save, reset output and counter
48
                pos_color <= led_positions;</pre>
49
                 count <= 0;</pre>
50
                 done <= 0;
51
                 thumb <= 0;
52
                 index <= 0;
53
                middle <= 0;
54
                 ring <= 0;
55
                pinkie <= 0;</pre>
56
                palm <= 0;</pre>
```

```
57
                  pos0 <= led_positions[0][8:0]; //separates array into variables</pre>
58
                  col0 <= led_positions[0][11:9];</pre>
59
                  pos1 <= led_positions[1][8:0];</pre>
60
                  col1 <= led_positions[1][11:9];</pre>
61
                  pos2 <= led_positions[2][8:0];</pre>
62
                  col2 <= led_positions[2][11:9];</pre>
63
                  pos3 <= led_positions[3][8:0];</pre>
64
                  col3 <= led_positions[3][11:9];</pre>
65
                  pos4 <= led_positions[4][8:0];</pre>
66
                  col4 <= led_positions[4][11:9];</pre>
67
                  pos5 <= led_positions[5][8:0];</pre>
68
                  col5 <= led_positions[5][11:9];</pre>
69
70
               //look at first item in array
71
             end else if (count == 0) begin
72
                  count <= count + 1;</pre>
73
                  if(col0 == GREEN) begin //green light = middle finger
74
                      middle <= pos0;
75
                  end else if (col0 == ORANGE) begin //orange light = index finger
76
                      index <= pos0;
77
                  end else if (col0 == BLUE) begin //blue light = thumb (for now)
78
                      thumb <= pos0;
79
                  end else if (col0 == RED) begin //red light = palm (for now)
80
                      palm <= pos0;</pre>
81
                  end else begin //no color, we're done
82
                      count <= 6;
83
                      done <= 1;
84
                  end
85
86
               //second item in array, index 1
87
             end else if (count == 1) begin
88
                  count <= count + 1;</pre>
89
                  if (col1 == GREEN) begin //green light = middle finger
90
                      middle <= pos1;</pre>
91
                  end else if (col1 == ORANGE) begin //orange light = index finger
92
                      index <= pos1;</pre>
93
                  end else if(col1 == BLUE) begin //blue light
94
                      if(thumb == 0) begin //first blue light = thumb
95
                           thumb <= pos1;</pre>
96
                      end else if (pos1[8:4] > thumb[8:4]) begin //this light left of
                          previous, new thumb
97
                           pinkie <= thumb;</pre>
98
                           thumb <= pos1;</pre>
99
                      end else begin //this light is to the right, is the pinkie
100
                           pinkie <= pos1;</pre>
101
                      end
102
                  end else if (col1 == RED) begin //red light
                      if (palm == 0) begin //first red light = palm
103
104
                           palm <= pos1;</pre>
105
                      end else if (pos1[3:0] > palm[3:0]) begin //this light is lower than
                           previous, new palm
106
                           ring <= palm;</pre>
107
                           palm <= pos1;</pre>
108
                      end else begin // this light is above the previous, ring finger
109
                           ring <= pos1;</pre>
110
                      end
111
                  end else begin //no color, we're done
112
                      count <= 6;
113
                      done <= 1;
```

114	end
115	
110	//LNIrd item in array, index 2
11/	end else if (count == 2) begin
118	count <= count + 1;
119	if (col2 == GREEN) begin //green light = middle finger
120	midale <= posz;
121	end else if(col2 == ORANGE) begin //orange light = index finger
122	index <= posz;
123	end else if(col2 == BLUE) begin //blue light
124	ii(thumb == 0) begin //first blue light = thumb
125	thumb <= pos_2 ;
120	end else if(pos2[8:4] > thumb[8:4]) begin //this light left of
107	previous, new chumb
127	pinkie <= thumb;
120	chumb <= posz;
129	end else begin //this light is to the right, is the pinkle
130	pinkie <- posz;
131	end
132	if (polm == 0) bogin //first rod light = polm
137	$\frac{11}{palm} = 0) begin //11st red right - palm$
134	paim $\sim pos_2$, and also if $(pos_2[3:0] > polm[3:0])$ begin $//this light is lower than$
155	provious now palm
136	$ring \leq ralm$
137	$ralm \leq ros^2$.
138	end else begin // this light is above the previous, ring finger
139	ring <= pos?.
140	end
141	end else begin //no color, we're done
142	$count \leq 6$:
143	done <= 1:
144	end
145	
146	//fourth item in array, index 3
147	end else if (count == 3) begin
148	count <= count + 1;
149	if(col3 == GREEN) begin //green light = middle finger
150	middle <= pos3;
151	end else if(col3 == ORANGE) begin //orange light = index finger
152	index <= pos3;
153	end else if(col3 == BLUE) begin //blue light
154	if(thumb == 0) begin //first blue light = thumb
155	thumb <= pos3;
156	end else if(pos3[8:4] > thumb[8:4]) begin //this light left of
	previous, new thumb
157	pinkie <= thumb;
158	thumb <= pos3;
159	end else begin //this light is to the right, is the pinkie
160	pinkie <= pos3;
161	end
162	end else if (col3 == RED) begin //red light
163	if (palm == 0) begin //first red light = palm
164	palm <= pos3;
165	<pre>end else if(pos3[3:0] > palm[3:0]) begin //this light is lower than</pre>
166	previous, new palm
100	ring <= palm;
10/	palm <= pos3;
168	end else begin // this light is above the previous, ring finger

```
169
                          ring <= pos3;
170
                      end
171
                 end else begin //no color, we're done
172
                      count <= 6;
173
                      done <= 1;
174
                 end
175
             //fifth item in array, index 4
176
177
             end else if (count == 4) begin
178
                 count <= count + 1;</pre>
179
                 if (col4 == GREEN) begin //green light = middle finger
180
                      middle <= pos4;
181
                 end else if (col4 == ORANGE) begin //orange light = index finger
182
                      index <= pos4;
183
                 end else if (col4 == BLUE) begin //blue light
184
                      if(thumb == 0) begin //first blue light = thumb
185
                          thumb <= pos4;
186
                      end else if (pos4[8:4] > thumb[8:4]) begin //this light left of
                         previous, new thumb
187
                          pinkie <= thumb;</pre>
188
                          thumb <= pos4;
189
                      end else begin //this light is to the right, is the pinkie
190
                          pinkie <= pos4;</pre>
191
                      end
192
                 end else if (col4 == RED) begin //red light
193
                      if (palm == 0) begin //first red light = palm
194
                          palm <= pos4;</pre>
195
                      end else if (pos4[3:0] > palm[3:0]) begin //this light is lower than
                          previous, new palm
196
                          ring <= palm;</pre>
197
                          palm <= pos4;</pre>
198
                      end else begin // this light is above the previous, ring finger
199
                          ring <= pos4;
200
                      end
201
                 end else begin //no color, we're done
202
                      count <= 6;
203
                      done <= 1;
204
                  end
205
206
             //sixth item in array, index 5
207
             end else if (count == 5) begin
208
                 count <= count + 1;</pre>
209
                 if(col5 == GREEN) begin //green light = middle finger
210
                      middle <= pos5;</pre>
211
                 end else if (col5 == ORANGE) begin //orange light = index finger
212
                      index <= pos5;
                 end else if (col5 == BLUE) begin //blue light
213
                      if(thumb == 0) begin //first blue light = thumb
214
215
                          thumb <= pos5;
216
                      end else if(pos5[8:4] > thumb[8:4]) begin //this light left of
                          previous, new thumb
217
                          pinkie <= thumb;</pre>
218
                          thumb <= pos5;
219
                      end else begin //this light is to the right, is the pinkie
220
                          pinkie <= pos5;</pre>
221
                      end
222
                 end else if (col5 == RED) begin //red light
223
                      if (palm == 0) begin //first red light = palm
224
                          palm <= pos5;</pre>
```

225	end else if(pos5[3:0] > palm[3:0]) begin //this light is lower than
	previous, new palm
226	<pre>ring <= palm;</pre>
227	palm <= pos5;
228	end else begin // this light is above the previous, ring finger
229	ring <= pos5;
230	end
231	end else begin //no color, we're done
232	count <= 6;
233	done <= 1;
234	end
235	done <= 1; //set done to 1 to signal we've finished
236	
237	<pre>//count > 5, all inputs have been processed</pre>
238	end else begin
239	done <= 0; //reset done signal
240	end
241	
242	end
243	endmodule

Gesture Recognition

```
'timescale 1ns / 1ps
1
2
3 module gesture_recognition_fsm(
4
                                     input clock,
5
                                     input reset,
6
                                     input [8:0] palm, //palm
7
                                     input [8:0] thumb, //thumb
8
                                     input [8:0] index, //index
9
                                     input [8:0] middle, //middle
10
                                     input [8:0] ring, //ring
11
                                     input [8:0] pinkie, //pinkie
12
                                     input start,
13
                                     output logic [4:0] letter,
14
                                     output logic done
15
       );
16
       parameter IDLE = 'b000;
17
       parameter LED_DET = 'b001;
18
       parameter STAT_REC = 'b011;
19
       parameter DYNAMIC = 'b111;
20
       parameter DEB = 'b010;
21
22
       parameter A_XDIFF1 = 'd7;
23
       parameter T_E_XDIFF1 = 'd0;
24
       parameter S_XDIFF1 = 'd4;
25
       parameter E_YDIFF1 = 'd5;
26
       parameter V_XDIFF1 = 'd3;
27
       parameter K_YDIFF1 = 'd6;
28
       parameter O_YDIFF1 = 'd5;
29
       parameter G_YDIFF1 = 'd3;
30
       parameter H_YDIFF = 'd3;
31
       parameter H_XDIFF = 'd10;
32
33
       parameter DEB_COUNT = 'd3;
       parameter J_TIME = 195_000_000;
34
35
36
       logic [4:0] prev_letter;
37
       logic [8:0] count;
38
39
       logic i;
40
       logic d;
41
       logic z;
42
       logic z_flag;
43
       logic [27:0] z_count;
44
       logic j;
45
       logic j_flag;
46
       logic [27:0] j_count;
47
       logic [2:0] state;
48
49
       is_jay jay (.clock(clock),.reset(reset),.start(i),.done(j), .letter(letter), .
           fsm_done(done),
50
                    .palm(palm),.thumb(thumb),.index(index),.middle(middle),.ring(ring),.
                        pinkie(pinkie));
51
52
       is_zee zee (.clock(clock),.reset(reset),.start(d),.done(z),.letter(letter),.
           fsm_done(done),
53
                    .palm(palm),.thumb(thumb),.index(index),.middle(middle),.ring(ring),.
```

```
pinkie(pinkie));
54
55
         always_ff @ (posedge clock) begin
56
             if(reset) begin //reset signal
57
                 done <= 0;
58
                 state <= IDLE;</pre>
59
                 i <= 0;
60
                 d <= 0;
61
                 z_count <= 0;
62
                 z_flag <= 0;</pre>
63
                 j_count <= 0;
64
                 j_flag <= 0;
65
             end else if (j || j_flag) begin //is_jay module signals j has been signed
                                                //and we haven't displayed long enough yet
66
                 i <= 0;
67
                 letter <= 5' d10;
68
                 done <= 1;
69
                 if(j_count == J_TIME) begin //we have displayed for J_TIME cycles, stop
70
                      j_flag <= 0;
71
                      j_count <= 0;
72
                 end else begin //continue displaying, increment time
73
                      j_count <= j_count + 1;</pre>
                      j_flag <= 1;
74
75
                 end
76
             end else if (z || z_flag) begin //is_zee module signals z has been signed
77
                 d <= 0;
                                                //and we haven't displayed long enough yet
78
                 letter <= 5' d26;
79
                 done <= 1;
80
                 if (z_count == J_TIME) begin //we have displayed for J_TIME cycles, stop
81
                      z_flag <= 0;</pre>
82
                      z_count <= 0;</pre>
83
                 end else begin //continue displaying, increment time
84
                      z_count <= z_count + 1;</pre>
85
                      z_flag <= 1;</pre>
86
                 end
87
88
             end else begin //state machine for static recognition
89
                 case (state)
                      IDLE: begin //idle state waits for valid LED positions
90
91
                          done <= 0;
92
                          i <= 0;
93
                          d <= 0;
94
                          if(start) begin //positions are valid
95
                              state <= STAT_REC;</pre>
96
                          end
97
                      end
98
99
                      STAT_REC: begin //identify letter being signed
100
                          //a, e, t, s, or n
101
                          if (index == 0 && middle == 0 && ring == 0 && pinkie == 0 && thumb
                               != 0 && palm != 0) begin
102
                               if(thumb[8:4] > palm[8:4] + A_XDIFF1) begin //a
                                   letter <= 5'd1;</pre>
103
104
                                   state <= DEB;</pre>
105
                               end else if(thumb[8:4] > palm[8:4] + S_XDIFF1) begin //s
106
                                   letter <= 5'd19;</pre>
107
                                   state <= DEB;</pre>
108
                               end else if(thumb[8:4] > palm[8:4] + T_E_XDIFF1) begin //e or
                                   t
109
                                   if (thumb[3:0] + E_YDIFF1 < palm[3:0]) begin //t</pre>
```

110	letter <= 5'd20;
111	<pre>state <= DEB;</pre>
112	end else begin //e
113	letter <= 5'd5;
114	state <= DEB;
115	end
116	end else begin //n
117	letter <= 5'd14;
118	state <= DEB;
119	end
120	//space
121	end else if (index == 0 && middle == 0 && ring == 0 && pinkie == 0
	&& thumb == 0 && palm == 0) begin
122	letter <= 0;
123	<pre>state <= DEB;</pre>
124	//m
125	end else if (index == 0 && middle == 0 && ring == 0 && pinkie == 0
	&& thumb == 0 && palm != 0) begin
126	letter <= 5'd13:
127	state <= DEB:
128	//f
129	end else if (index == 0 & middle != 0 & ring != 0 & pinkie != 0
	κ thumb $l = 0$ κ palm $l = 0$ begin
130	letter $\leq 5'$ d6:
131	state <= DEB:
132	//v or i
132	end else if (index == 0 && middle == 0 && ring == 0 && pinkie != 0
155	$ \begin{array}{c} \begin{array}{c} \begin{array}{c} \begin{array}{c} \end{array} \\ \end{array} \end{array} \\ $
134	if (thumb[8:4] > $palm(8:4] + A VDIFF1) begin //w$
135	letter $\leq 5' d^{25}$.
136	state $\leq DEB$.
130	and also begin //i
139	
130	stato <= DER:
140	and
140	$\frac{1}{2}$
141	//q, g , I , u , OIX
172	end else il (index := 0 & a middle == 0 & a fing == 0 & a pinkle == 0 $(t + t)$
1/13	$\alpha \alpha$ chumb := 0 $\alpha \alpha$ parm := 0) begin if (thumb[2:0] > palm[2:0]) begin (/ α
143	$\frac{1}{10000000000000000000000000000000000$
144	atata <- DED:
145	state \sim DED, and also if (thumb[2:0] < index[2:0] + C VDIFE1) begin $//a$
140	end else il(chumb[s:0] < index[s:0] + G_iDIFFI) begin $7/g$
147	ieter <- DEP.
140	State $<-$ DED;
149	end else il(chumb[8:4] > palm[8:4] + A_XDIFFI) begin //1
150	letter <= 5'dl2;
151	state <= DEB;
152	end else if (thumb[8:4] > paim[8:4] + 1_E_XDIFFI) begin //d
153	letter <= 5'04;
154	state <= DEB;
155	end else begin //x
150	letter <= 5'd24;
13/	state <= DEB;
158	end
159	//c, o or b
100	end else it (index != 0 && middle != 0 && ring != 0 && pinkie != 0
171	&& thumb != 0 && palm != 0) begin
101	if(thumb[8:4] > palm[8:4] + A_XDIFF1) begin //c
162	letter <= 5'd3;

163	<pre>state <= DEB;</pre>
164	end else if (thumb[3:0] + O_YDIFF1 < palm[3:0]) begin //o
165	<pre>letter <= 5'd15;</pre>
166	<pre>state <= DEB;</pre>
167	end else begin //b
168	letter $\leq 5' d_2$;
169	state <= DEB:
170	end
171	
172	and old if (index $l = 0$ is middle $l = 0$ is ring $ 0$ is pinkin $ 0$
172	f_{1} end end f_{1} index f_{2} to g_{2} initially f_{2} and f_{1} index f_{2} to g_{2} initially f_{2} to g_{2} printle f_{2} of g_{2} printle f_{2} printle f_{2
173	$\alpha \alpha$ clumb := 0 $\alpha \alpha$ paim := 0) begin if (middle[9:4] + H XDIFE < palm[9:4]) begin
173	lit (middle[c:4] + n_ADIFr < palm(c:4]) begin
174	fetter <= 520
175	state <= DEB;
176	end else if (middle[3:0] > thumb[3:0]) begin $//p$
177	letter <= 5'dl6;
178	state <= DEB;
179	end else if (thumb[3:0] + K_YDIFF1 < palm[3:0]) begin //k
180	letter <= 5'dl1;
181	state <= DEB;
182	end else if(middle[8:4] > index[8:4]) begin//r
183	<pre>letter <= 5'd18;</pre>
184	<pre>state <= DEB;</pre>
185	end else if (index[8:4] > middle[8:4] + V XDIFF1) begin $//v$
186	letter <= 5'd22;
187	state <= DEB:
188	end else begin //u
189	letter $\leq 5/421$.
190	state <= DER.
101	ord
102	
192	//w
195	end else il (Index := 0 && middle := 0 && ring := 0 && pinkle == 0
104	&& paim := 0) begin
194	letter ≤ 5 d23;
195	state <= DEB;
196	//no sign
197	end else begin
198	letter <= 0;
199	state <= IDLE;
200	end
201	end
202	
203	DEB: begin //checks sign is consistent for long enough and sets done
	signal for display
204	if(letter == prev_letter) begin //consistent
205	if(count == DEB COUNT) begin //for long enough, trigger
	display
206	done $\leq = 1$:
207	coupt <= 0:
208	if (letter == 9) begin $//triggers is iaw module$
200	i (i c - 1.
209	$1 \sim 1$,
210 211	end eise if (fetter == 4) begin //triggers is_zee module
∠11 212	$\alpha \leq 1;$
212	ena
213	end else begin
214	<pre>count <= count + 1;</pre>
215	end
216	end else begin //new letter, reset count, no done signal to
	display

217 count <= 0; 218 end 219 prev_letter <= letter;</pre> 220 state <= IDLE;</pre> 221 end 222 endcase 223 end 224 end 225 endmodule

J Identification

```
1
   module is_jay (
2
                    input clock,
                    input reset,
3
4
                    input start,
5
                    input fsm_done,
6
                    input [4:0] letter,
7
                    input [8:0] palm, //palm
8
                    input [8:0] thumb, //thumb
9
                    input [8:0] index, //index
10
                    input [8:0] middle, //middle
11
                    input [8:0] ring, //ring
12
                    input [8:0] pinkie, //pinkie
13
                    output logic done
14
       );
15
        localparam VALID_Y_SHIFT = 1;
                                                      //downward shift necessary to register
            as having moved down
16
        localparam VALID_X_SHIFT = 8;
                                                      //rightward shift necessary to
           register as having moved right
17
18
        localparam IDLE = 2'b00;
19
        localparam ACTIVATED = 2'b01;
20
        localparam SHIFTED_DOWN = 2'b11;
21
        localparam FINISHED = 2'b10;
22
23
                                                      //number of bins in the x direction
        localparam BIN_X_WIDTH = 31;
24
        localparam BIN_Y_HEIGHT = 15;
                                                      //number of bins in the y direction
25
26
        logic [1:0] recognition_state;
27
        logic [8:0] last_pinkie;
                                                      //first recognized position of pinkie
            when starting the module
28
        logic [8:0] last_thumb;
                                                      //first recognized position of thumb
            when starting the module
29
        logic [8:0] actually_pinkie;
                                                      //variable used for clarity
30
31
        //due to necessary previous design choices, the thumb position that is passed to
            this module while signing
32
        //j will actually be the pinkie of the user, as such, this is an alias for the
            thumb
33
        assign actually_pinkie = thumb[8:0];
34
35
        always_ff @(posedge clock) begin
36
            if (reset) begin
37
                done <= 0;</pre>
38
                recognition_state <= IDLE;
39
            end else begin
40
                case (recognition_state)
41
                                    begin
                    TDLE
                                 :
42
                                         if (start) begin
                                                                                    //once a "
                                             i" has been recognized
43
                                              recognition_state <= ACTIVATED;</pre>
44
                                              last_pinkie <= pinkie;</pre>
45
                                              last_thumb <= thumb;</pre>
46
                                         end
47
                                         done <= 0;
48
                                     end
49
                    ACTIVATED
                                     begin
                                 :
```

50		if (letter != 9 && fsm_done) begin //reset if
51		another letter is recognized
51		recognition_state <= IDLE;
52		end else if (last_tnumb[3:0] < BIN_Y_HEIGHI -
		VALID_Y_SHIFT) begin
53		<pre>//if the pinkie has moved far enough down,</pre>
		proceed
54		if (actually_pinkie[3:0] >= last_thumb[3:0] +
		VALID_Y_SHIFT) recognition_state <=
		SHIFTED_DOWN;
55		end else recognition_state <= IDLE;
56		end
57	SHIFTED_DOWN:	begin
58		if (letter != 9 && fsm_done) begin
59		recognition state <= IDLE;
60		end else if ((last pinkie[8:4] < BIN X WIDTH -
		VALID X SHIFT)) begin
61		//go to FINISHED state if pinkie moves right
01		enough
62		if $(actually pinkie[8.4] >= last pinkie[8.4] +$
02		WILD X SHIFT) recognition state <=
		FINISUED.
63		and also recognition state $\zeta = IDLE$.
64		and
65	EINITGUED	ena
05	FINISHED :	Degin
00		aone <= 1;
6/		recognition_state <= IDLE;
68		end
69	endcase	
70	end	
71	end	
72	endmodule	

Z Identification

```
module is_zee (
1
2
                    input clock,
3
                    input reset,
4
                    input start,
5
                    input fsm_done,
6
                    input [4:0] letter,
7
                    input [8:0] palm, //palm
8
                    input [8:0] thumb, //thumb
9
                    input [8:0] index, //index
10
                    input [8:0] middle, //middle
11
                    input [8:0] ring, //ring
12
                    input [8:0] pinkie, //pinkie
13
                    output logic done
14
        );
15
16
        //respective shifts required to register in a certain direction
17
        localparam VALID_LEFT_SHIFT = 8;
18
        localparam VALID_DOWN_SHIFT = 5;
19
20
        localparam IDLE = 3'b000;
        localparam ACTIVATED = 3'b001;
21
22
        localparam SHIFTED_LEFT = 3'b011;
23
        localparam SHIFTED_DOWN_RIGHT = 3'b111;
24
        localparam FINISHED = 3'b110;
25
26
        localparam BIN_X_WIDTH = 31;
27
        localparam BIN_Y_HEIGHT = 15;
28
29
        logic [2:0] recognition_state;
30
        logic [8:0] last_index;
31
32
        always_ff @(posedge clock) begin
33
            if (reset) begin
34
                done <= 0;
35
                recognition_state <= IDLE;
36
            end else begin
37
                case (recognition_state)
38
                    IDLE
                                          :
                                              begin
39
                                                  if (start) begin
                                                                                         //once
                                                        a "d" has been recognized
40
                                                       recognition_state <= ACTIVATED;</pre>
41
                                                       last_index <= index;</pre>
42
                                                   end
43
                                                   done <= 0;
44
                                              end
45
                    ACTIVATED
                                              begin
                                          :
46
                                                   if (letter != 4 && fsm_done)
                                                       recognition_state <= IDLE;</pre>
47
                                                   else if ((last_index[8:4] >
                                                      VALID_LEFT_SHIFT) && (last_index[3:0] <</pre>
                                                       BIN_Y_HEIGHT - VALID_DOWN_SHIFT))
                                                      begin
48
                                                       //if the index finger has moved far
                                                           enough left, proceed
49
                                                       //the index != 0 check has been added
                                                           to account for position instability
```

50			<pre>if ((index[8:4] <= last_index[8:4] - VALID_LEFT_SHIFT) && index[8:4] != 0) recognition_state <=</pre>
			SHIFTED_LEFT;
51			<pre>end else recognition_state <= IDLE;</pre>
52			end
53	SHIFTED LEFT	:	begin
54	—		if (letter != 4 && fsm done)
			recognition state <= IDLE;
55			//if the index finger has moved far enough
			down and right, proceed
56			else if $((index[8:4]) \ge last index[8:4])$
00			$\mathcal{L} = \left\{ \frac{1}{2} + \frac{1}$
			VALID DOWN SHIFT)) recognition state <=
			SHIFTED DOWN RIGHT.
57			end
58	SHIFTED DOWN RIGHT		begin
59		•	if (letter $l = 4 &&$ fsm done)
57			recognition state \leq IDLF.
60			//if the index finger has meyed left
00			opough finish
61			olso if ((indox[8:4] <= last indox[8:4] =
01			VALUE LEET CULET) (C index[0.4] (- 0)
			valid_lift[_Shirt] && Index[0.4] := 0)
62			and
62	EINICHED		begin
64	FINISHED	•	dono <= 1.
65			done <-1 ,
66			recognition_state <= IDLE;
67	dofoult .		ena
68	ueraurt : ;		
60	enucase		
70	ena		
70	ena		

71 endmodule

Text Display

```
1
   module text_display (
2
                         input pixel_clk_in,
3
                         input reset,
4
                         input [10:0] hcount_in,
5
                         input [9:0] vcount_in,
6
                         input [4:0] letter,
7
                         input done,
8
                         input confirm_letter,
9
                         input delete_letter,
10
                         output logic [11:0] pixel_out);
11
12
        integer i;
13
14
        logic [3:0] start_offset;
                                                       // num of bits for 256*240 ROM
15
        logic [10:0] start_addr;
16
        logic [7:0] char_row;
                                                       //ROM output as 8 bit wide array
17
        logic [5:0] char_counter;
                                                       //tracks the current character being
           displayed
18
        logic [7:0] current_row;
                                                       //current char_row being used for
           display
19
        logic [4:0] current_letter;
                                                       //current letter being display
20
        logic [4:0] letter_array [63:0];
                                                       //array which holds 5 bit numbers
           corresponding to letters
21
        logic [2:0] row_counter;
                                                       //tracks the position in the current
           character row
22
        logic [5:0] letter_array_input_index;
                                                       //index of the next letter to be input
23
                                                       //addresses into the text ROM
        logic [10:0] text_rom_addr;
24
        logic started;
25
26
        letter_to_addr addr_finder (.letter(current_letter), .start_addr(start_addr));
27
28
        assign text_rom_addr = start_addr + start_offset;
29
30
31
        //instantiates ROM containing COE file for ASCII characters
32
        text_rom your_instance_name (
33
          .clka(pixel_clk_in),
34
          .addra(text_rom_addr),
35
          .douta(char_row)
36
       );
37
38
        always_ff @ (posedge pixel_clk_in) begin
39
            if (reset) begin
40
                start_offset <= 0;</pre>
41
                char_counter <= 0;
42
                current_row <= 0;</pre>
43
                row_counter <= 0;</pre>
44
                started <= 0;</pre>
45
                letter_array_input_index <= 0;</pre>
46
47
                //resets all letters to empty spaces
48
                for (i=0; i<64; i=i+1) begin
49
                    letter_array[i] <= 5'b0;</pre>
50
                end
51
            end else begin
52
                if (vcount_in >= 240 && vcount_in < 255) begin
                                                                                //when the
```

	currently displayed pixel line is within this module's domain
53	if (hcount_in < 512) begin //when within
	the range of the text (8x64=512)
54	if (char_counter == 63 && row_counter == 4) begin
	reached the end of the character line; occurs 3 cycles in advance
55	<pre>start_offset <= start_offset + 1;</pre>
56	char_counter <= 0;
57	<pre>current_letter <= letter_array[0];</pre>
58	<pre>end else if (row_counter == 4) begin //occurs 3 cycles in advance to account for ROM read latency</pre>
59	<pre>current_letter <= letter_array[char_counter + 1];</pre>
60	<pre>char_counter <= char_counter + 1;</pre>
61	end
62	if (row_counter == 7) begin //actually finished displaying the current character
63	<pre>current_row <= char_row;</pre>
64	end else if (hcount_in != 0)
65	shift in the next bit for the current character
66	pixel_out <= (current_row[7]) ? {12{1'b1}} : 12'b0;
67	<pre>row_counter <= (hcount_in==0) ? 0 : row_counter + 1;</pre>
68	end else begin //prepares for
	the next line in the frame
69	row_counter <= 0;
70	<pre>current_row <= char_row;</pre>
/1 72	end
12 73	and also havin
15	frame
74	current_row <= char_row;
75	current_letter <= letter_array[0];
76	<pre>start_offset <= 0;</pre>
77	<pre>pixel_out <= 12'b0;</pre>
78	end
79	end
80	<pre>if (done && !delete_letter) begin</pre>
81	<pre>letter_array[letter_array_input_index] <= letter;</pre>
82	end
83	if (confirm_letter) begin //increment the cursor, leaving the current letter at the previous index
84	<pre>letter_array_input_index <= letter_array_input_index + 1;</pre>
85	end else if (delete_letter) begin //decrement the cursor, deleting
86	letter arrav[letter array input index] <= 0.
87	letter_array_input_index <= (letter_array_input_index == 0) ? 0 ·
07	letter array input index - 1:
88	end
89	end
90	endmodule
91	
92	module letter_to_addr (
93	input [4:0] letter,
94	output logic [10:0] start_addr
95);
96	parameter START_OFFSET = 1024; //offset to the start of the caps alphabet;
97	start at ascii value = 00 parameter SPACE VALUE = 0.
11	parameter brach_value - v,

98 always_comb begin 99 start_addr = (letter != SPACE_VALUE) ? (letter * 16) + START_OFFSET : 0; 100 end 101 102 endmodule

Camera Control

```
1
   /*
 2
      Wire - I2C Scanner
 3
      The WeMos D1 Mini I2C bus uses pins:
 4
      D1 = SCL
 5
      D2 = SDA
 6
   */
 7
 8
   #include <Wire.h>
 9
10 /*These are settings some of which have been found empirically and/or found
11
   from random internet sites. When you see that there's a "magic" number it
12 isn't a magic number like in comp sci or something...it just means we have
13 no idea why this register value seems to help since the data sheet doesn't
14 give a ton of guidance. I'm sure there's rational explanations for many of
15 these numbers, but sometimes I've just got bills to pay and life to live
16 and don't have time to figure out why. You know the deal.
17 */
18
19
   const byte ADDR = 0x21; //name of the camera on I2C
20
21 uint8_t settings[][2] = {
22
     {0x12, 0x80}, //reset
      {OxFF, OxFO}, //delay
23
24
     {0x12, 0x14}, // COM7,
                                 set RGB color output (QVGA and test pattern 0x6...for RGB
          video 0x4)
25
     {0x11, 0x80}, // CLKRC
                                 internal PLL matches input clock
26
     {0x0C, 0x00}, // COM3,
                                 default settings
27
     {0x3E, 0x00}, // COM14,
                                 no scaling, normal pclock
28
     {0x04, 0x00}, // COM1,
                                 disable CCIR656
29
     {0x40, 0xd0}, //COM15,
                                 RGB565, full output range
30
     {0x3a, 0x04}, //TSLB
                                 set correct output data sequence (magic)
     {0x14, 0x18}, //COM9
31
                                 MAX AGC value x4
32
     {0x4F, 0xB3}, //MTX1
                                 all of these are magical matrix coefficients
     {0x50, 0xB3}, //MTX2
33
34
     {0x51, 0x00}, //MTX3
     {0x52, 0x3d}, //MTX4
35
36
     {0x53, 0xA7}, //MTX5
37
     {0x54, 0xE4}, //MTX6
     {0x58, 0x9E}, //MTXS
38
39
     {0x3D, 0xC0}, //COM13
                                 sets gamma enable, does not preserve reserved bits, may
         be wrong?
40
     {0x17, 0x14}, //HSTART
                                 start high 8 bits
41
     {0x18, 0x02}, //HSTOP
                                 stop high 8 bits //these kill the odd colored line
42
     {0x32, 0x80}, //HREF
                                 edge offset
     {0x19, 0x03}, //VSTART
43
                                 start high 8 bits
44
     {0x1A, 0x7B}, //VSTOP
                                 stop high 8 bits
45
     {0x03, 0x0A}, //VREF
                                 vsync edge offset
46
     {0x0F, 0x41}, //COM6
                                 reset timings
      {0x1E, 0x00}, //MVFP
47
                                 disable mirror / flip //might have magic value of 03
      {0x33, 0x0B}, //CHLF
48
                                 //magic value from the internet
49
      {0x3C, 0x78}, //COM12
                                 no HREF when VSYNC low
50
     {0x69, 0x00}, //GFIX
                                 fix gain control
51
     {0x74, 0x00}, //REG74
                                 Digital gain control
52
     {0xB0, 0x84}, //RSVD
                                 magic value from the internet *required* for good color
53
     {0xB1, 0x0c}, //ABLC1
54
     {0xB2, 0x0e}, //RSVD
                                 more magic internet values
```

```
55
       {0xB3, 0x80}, //THL_ST
56
      //begin mystery scaling numbers. Thanks, internet!
57
      {0x70, 0x3a},
58
      {0x71, 0x35},
59
      {0x72, 0x11},
60
      {0x73, 0xf0},
61
      \{0xa2, 0x02\},\
      //gamma curve values
62
63
      \{0x7a, 0x20\},\
      {0x7b, 0x10},
64
      {0x7c, 0x1e},
65
      {0x7d, 0x35},
66
67
      {0x7e, 0x5a},
68
      {0x7f, 0x69},
69
      \{0x80, 0x76\},\
 70
      {0x81, 0x80},
71
      {0x82, 0x88},
72
      {0x83, 0x8f},
73
      {0x84, 0x96},
74
      {0x85, 0xa3},
75
      {0x86, 0xaf},
76
      {0x87, 0xc4},
77
      {0x88, 0xd7},
78
      {0x89, 0xe8},
79
      //WB Stuff (new stuff!!!!)
80
      {0x00, 0x00}, //set gain reg to 0 for AGC
81
      {0x01, 0x8F}, //blue gain (default 80)
82
      //{0x02, 0x8F}, //reg gain (default 80)
83
      {0x02, 0x80}, //reg gain (default 80)
84
      {0x6a, 0x3F}, //green gain (default not sure!)
85
      //{0x6a, 0x4F}, //green gain (default not sure!)
86
      {0x13, 0x00}, //disable all automatic features!! (including automatic white balance)
87
      {0x10,0x20}, //exposure (default 40)
88
      //added value
89
      {0x55, 0x44}, //increases brightness
90
      {0xA0, 0xFF}, //increases brightness
91
      {0x9F, 0xFF}, //increases brightness
92
      //{0x55, 0x2F}, //increases brightness
93
      //{0xA0, 0xFF}, //increases brightness
94
      //{0x9F, 0xFF}, //increases brightness
95 };
96 uint8_t output_state;
97
98 void setup()
99 {
100
      Wire.begin();
      Serial.begin(115200);
101
102
      Serial.println("Starting");
103
      delay(1000);
104
      Wire.beginTransmission(ADDR);
105
      Wire.write(0x0A);
106
      Wire.requestFrom(ADDR, 2);
107
      byte LSB = Wire.read();
108
      byte MSB = Wire.read();
109
      uint16_t val = ((MSB << 8) | LSB);
110
      Wire.endTransmission();
111
      Serial.println(val);
112
      for (int i = 0; i < sizeof(settings) / 2; i++) {</pre>
113
        Wire.beginTransmission(ADDR);
```

```
114
        Wire.write(settings[i][0]);
115
        Wire.write(settings[i][1]);
        //
116
              Wire.write(RegValues[i][1]);
117
        11
              Wire.write(RegValues[i][2]);
118
        Wire.endTransmission();
119
      }
120
      // Wire.write(0x12);
121
      // Wire.write(0x4);
122
      Serial.println("OV7670 Setup Done");
123
      pinMode(4, INPUT_PULLUP);
124
      output_state = 0;
125 }
126
127
128 void loop()
129 {
130
131 }
132
133
134 void writeByte(uint8_t target_reg, uint8_t val) {
      Wire.beginTransmission(ADDR);
135
136
      Wire.write(target_reg);
137
      Wire.write(val);
138
      Wire.endTransmission();
139 }
140
141 void readBytes(uint8_t target_reg, uint8_t* val_out, uint8_t num_bytes) {
142
      Wire.beginTransmission(ADDR);
143
      Wire.write(target_reg);
144
      Wire.requestFrom(ADDR, num_bytes);
145
      uint8_t* ptr_to_out;
146
      ptr_to_out = val_out;
147
      for (int i = 0; i < num_bytes; i++) {</pre>
148
        *ptr_to_out = Wire.read();
149
        ptr_to_out++;
150
      }
151 }
```